



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Revaluation/Recounting results for M.Tech (R13/R09)II SEMESTER REG/SUPPLE EXAMINATIONS Oct-2015

College name: ST.MARYS GROUP OF INSTITUTIONS, CHEBROLU, GUNTUR:BJ

| Htno       | Subcode | Subname                        | Internal | External | credits | STATUS      |
|------------|---------|--------------------------------|----------|----------|---------|-------------|
| 13BJ1D0405 | H0401   | SIMULATION MODELING OF MANUFAC | 35       | 17       | 0       | Not Applied |
| 13BJ1D0405 | H2103   | FINITE ELEMENT METHOD          | 34       | 10       | 0       | Not Applied |
| 13BJ1D0406 | H2103   | FINITE ELEMENT METHOD          | 34       | 17       | 0       | Not Applied |
| 13BJ1D0407 | H2103   | FINITE ELEMENT METHOD          | 36       | 6        | 0       | Not Applied |
| 13BJ1D0408 | H2103   | FINITE ELEMENT METHOD          | 36       | 20       | 0       | Not Applied |
| 13BJ1D0504 | H0502   | DESIGN AND ANALYSIS OF ALGORIT | 37       | 9        | 0       | Not Applied |
| 13BJ1D4306 | H4303   | DIGITAL CONTROLLERS            | 35       | 19       | 0       | Not Applied |
| 13BJ1D5502 | H6805   | DIGITAL SIGNAL PROCESSORS & AR | 35       | 17       | 0       | Not Applied |
| 13BJ1D5511 | H5501   | HARDWARE SOFTWARE CO-DESIGN    | 35       | 19       | 0       | Not Applied |
| 13BJ1D5512 | H5501   | HARDWARE SOFTWARE CO-DESIGN    | 35       | 15       | 0       | Not Applied |
| 13BJ1D5512 | H6805   | DIGITAL SIGNAL PROCESSORS & AR | 35       | 19       | 0       | Not Applied |
| 13BJ1D5512 | H6806   | SYSTEM ON CHIP DESIGN          | 34       | 18       | 0       | Not Applied |
| 13BJ1D5513 | H5501   | HARDWARE SOFTWARE CO-DESIGN    | 36       | 14       | 0       | Not Applied |
| 13BJ1D5514 | H5501   | HARDWARE SOFTWARE CO-DESIGN    | 35       | 17       | 0       | Not Applied |
| 13BJ1D5811 | H2508   | CLOUD COMPUTING                | 35       | 15       | 0       | Not Applied |
| 14BJ1D0404 | H0401   | SIMULATION MODELING OF MANUFAC | 37       | 12       | 0       | Not Applied |
| 14BJ1D0505 | H0502   | DESIGN AND ANALYSIS OF ALGORIT | 37       | 17       | 0       | Not Applied |
| 14BJ1D0505 | H0509   | WEB TECHNOLOGIES ELECTIVE-III  | 37       | 12       | 0       | Not Applied |
| 14BJ1D0506 | H0502   | DESIGN AND ANALYSIS OF ALGORIT | 35       | 12       | 0       | Not Applied |
| 14BJ1D0509 | H0501   | DATA WAREHOUSING AND DATA MINI | 35       | 15       | 0       | Not Applied |
| 14BJ1D0509 | H0502   | DESIGN AND ANALYSIS OF ALGORIT | 36       | 14       | 0       | Not Applied |
| 14BJ1D0516 | H0502   | DESIGN AND ANALYSIS OF ALGORIT | 35       | 5        | 0       | Not Applied |
| 14BJ1D2103 | H2102   | ENERGY MANAGEMENT              | 37       | 16       | 0       | Not Applied |
| 14BJ1D2103 | H2103   | FINITE ELEMENT METHOD          | 37       | 6        | 0       | Not Applied |
| 14BJ1D2107 | H2108   | ADVANCED AUTOMOBILE ENGG ELECT | 39       | 21       | 0       | Not Applied |
| 14BJ1D2109 | H2102   | ENERGY MANAGEMENT              | 39       | 19       | 0       | Not Applied |
| 14BJ1D2114 | H2103   | FINITE ELEMENT METHOD          | 39       | 16       | 0       | Not Applied |
| 14BJ1D2115 | H2102   | ENERGY MANAGEMENT              | 38       | 10       | 0       | Not Applied |
| 14BJ1D2118 | H2103   | FINITE ELEMENT METHOD          | 38       | 17       | 0       | Not Applied |
| 14BJ1D4305 | H4302   | ELECTRIC DRIVES-II             | 35       | 12       | 0       | Not Applied |
| 14BJ1D4305 | H4307   | ELECTRICAL DISTRIBUTION SYSTEM | 35       | 7        | 0       | Not Applied |
| 14BJ1D4315 | H4301   | SWITCHED MODE POWER CONVERSION | 33       | 9        | 0       | Not Applied |
| 14BJ1D5501 | H6806   | SYSTEM ON CHIP DESIGN          | 35       | 15       | 0       | Not Applied |
| 14BJ1D5509 | H5501   | HARDWARE SOFTWARE CO-DESIGN    | 36       | 16       | 0       | Not Applied |
| 14BJ1D5509 | H5503   | CPLD AND FPGA ARCHITECTURES AN | 36       | 19       | 0       | Not Applied |
| 14BJ1D5509 | H6805   | DIGITAL SIGNAL PROCESSORS & AR | 34       | 18       | 0       | Not Applied |
| 14BJ1D5509 | H6806   | SYSTEM ON CHIP DESIGN          | 36       | 7        | 0       | Not Applied |
| 14BJ1D5515 | H5501   | HARDWARE SOFTWARE CO-DESIGN    | 37       | 19       | 0       | Not Applied |
| 14BJ1D5515 | H5502   | EMBEDDED NETWORKING            | 37       | 15       | 0       | Not Applied |
| 14BJ1D5515 | H5503   | CPLD AND FPGA ARCHITECTURES AN | 38       | 19       | 0       | Not Applied |
| 14BJ1D5517 | H5501   | HARDWARE SOFTWARE CO-DESIGN    | 36       | 13       | 0       | Not Applied |
| 14BJ1D5517 | H6805   | DIGITAL SIGNAL PROCESSORS & AR | 34       | 17       | 0       | Not Applied |
| 14BJ1D5802 | H5803   | COMPILER DESIGN ELECTIVE-I     | 37       | 10       | 0       | Not Applied |
| 14BJ1D5805 | H5803   | COMPILER DESIGN ELECTIVE-I     | 37       | 12       | 0       | Not Applied |
| 14BJ1D5811 | H4002   | INFORMATION SECURITY           | 36       | 16       | 0       | Not Applied |
| 14BJ1D5811 | H5803   | COMPILER DESIGN ELECTIVE-I     | 36       | 3        | 0       | Not Applied |

| Htno       | Subcode | Subname                        | Internal | External | credits | STATUS      |
|------------|---------|--------------------------------|----------|----------|---------|-------------|
| 14BJ1D5811 | H5804   | OBJECT ORIENTED ANALYSIS AND D | 35       | 12       | 0       | Not Applied |
| 14BJ1D5815 | H5803   | COMPILER DESIGN ELECTIVE-I     | 35       | 1        | 0       | Not Applied |
| 14BJ1D8701 | H8703   | STABILITY OF STRUCTURES        | -2       | -2       | 0       | Not Applied |
| 14BJ1D8703 | H8701   | FINITE ELEMENT METHOD          | 37       | 26       | 1       | Not Applied |
| 14BJ1D8704 | H8703   | STABILITY OF STRUCTURES        | 37       | 14       | 0       | Not Applied |
| 14BJ1D8710 | H8703   | STABILITY OF STRUCTURES        | 38       | 8        | 0       | Not Applied |
| 14BJ1D8715 | H8703   | STABILITY OF STRUCTURES        | 38       | 14       | 0       | Not Applied |
| 14BJ1D8716 | H8702   | EARTHQUAKE RESISTANT DESIGN    | 35       | 20       | 0       | Not Applied |
| 14BJ1D8716 | H8703   | STABILITY OF STRUCTURES        | 36       | 7        | 0       | Not Applied |
| 14BJ1D8718 | H8703   | STABILITY OF STRUCTURES        | 37       | 13       | 0       | Not Applied |
| 13BJ1D4306 | H4301   | SWITCHED MODE POWER CONVERSION | 35       | 37       | 1       | Applied     |
| 13BJ1D5814 | H5801   | COMPUTER NETWORKS              | 33       | 25       | 1       | Applied     |
| 14BJ1D2102 | H2101   | FUELS COMBUSTION & ENVIRONMENT | 39       | 32       | 1       | Applied     |
| 14BJ1D2107 | H2101   | FUELS COMBUSTION & ENVIRONMENT | 37       | 25       | 1       | Applied     |
| 14BJ1D2107 | H2104   | COMPUTATIONAL FLUID DYNAMICS   | 37       | 26       | 1       | Applied     |
| 14BJ1D2109 | H2101   | FUELS COMBUSTION & ENVIRONMENT | 39       | 24       | 1       | Applied     |
| 14BJ1D2114 | H2101   | FUELS COMBUSTION & ENVIRONMENT | 38       | 24       | 1       | Applied     |
| 14BJ1D2115 | H2101   | FUELS COMBUSTION & ENVIRONMENT | 37       | 25       | 1       | Applied     |
| 14BJ1D2118 | H2101   | FUELS COMBUSTION & ENVIRONMENT | 39       | 26       | 1       | Applied     |
| 14BJ1D4305 | H4301   | SWITCHED MODE POWER CONVERSION | 37       | 28       | 1       | Applied     |
| 14BJ1D5802 | H4001   | ADVANCED UNIX PROGRAMMING      | 38       | 24       | 1       | Applied     |
| 14BJ1D5811 | H0501   | DATA WAREHOUSING AND DATA MINI | 35       | 26       | 1       | Applied     |
| 14BJ1D8703 | H8703   | STABILITY OF STRUCTURES        | 37       | 44       | 1       | Applied     |
| 14BJ1D8705 | H8703   | STABILITY OF STRUCTURES        | 38       | 30       | 1       | Applied     |
| 14BJ1D8716 | H8701   | FINITE ELEMENT METHOD          | 35       | 26       | 1       | Applied     |
| 14BJ1D8716 | H8704   | THEORY OF PLATES & SHELLS      | 37       | 28       | 1       | Applied     |
| 14BJ1D8716 | H8709   | BRIDGE ENGINEERING ELECTIVE-IV | 36       | 28       | 1       | Applied     |
| 14BJ1D8717 | H8704   | THEORY OF PLATES & SHELLS      | 36       | 31       | 1       | Applied     |

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Date:09-06-2016

Controller of Examinations