



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for M.Tech (R13/R09)II SEMESTER REG/SUPPLE EXAMINATIONS Oct-2015

College name: ST.MARYS GROUP OF INSTITUTIONS, CHEBROLU, GUNTUR:BJ

Discrepancy pertaining to this results supposed to be cleared on or before 18-02-2016 with following documents at CE Office,JNTUK,Kakinada

- Online Registration Proof
- Hallticket
- DForm(Online)
- DForm(Offline)
- Attendance Sheet
- Any Other supporting Documents

Htno	Subcode	Subname	Internal	External	credits
13BJ1D0402	H1501	OPTIMIZATION AND RELIABILITY	34	28	1
13BJ1D0402	H2103	FINITE ELEMENT METHOD	35	15	0
13BJ1D0405	H0401	SIMULATION MODELING OF MANUFACTURING SYS	35	17	0
13BJ1D0405	H2103	FINITE ELEMENT METHOD	34	10	0
13BJ1D0406	H2103	FINITE ELEMENT METHOD	34	17	0
13BJ1D0407	H2103	FINITE ELEMENT METHOD	36	6	0
13BJ1D0408	H0402	COMPUTER GRAPHICS	35	24	1
13BJ1D0408	H1501	OPTIMIZATION AND RELIABILITY	35	29	1
13BJ1D0408	H2103	FINITE ELEMENT METHOD	36	20	0
13BJ1D0410	H0401	SIMULATION MODELING OF MANUFACTURING SYS	33	9	0
13BJ1D0410	H0408	INTELLIGENT MANUFACTURING SYSTEMS	35	25	1
13BJ1D0410	H1501	OPTIMIZATION AND RELIABILITY	33	25	1
13BJ1D0410	H2103	FINITE ELEMENT METHOD	34	8	0
13BJ1D0412	H0401	SIMULATION MODELING OF MANUFACTURING SYS	34	-1	0
13BJ1D0412	H0402	COMPUTER GRAPHICS	35	-1	0
13BJ1D0412	H0404	CONCURRENT ENGINEERING ELECTIVE-III	34	-1	0
13BJ1D0412	H0408	INTELLIGENT MANUFACTURING SYSTEMS	33	-1	0
13BJ1D0412	H1501	OPTIMIZATION AND RELIABILITY	34	-1	0
13BJ1D0412	H2103	FINITE ELEMENT METHOD	34	-1	0
13BJ1D0504	H0501	DATA WAREHOUSING AND DATA MINING	36	24	1
13BJ1D0504	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	37	9	0
13BJ1D0504	H0507	HUMAN COMPUTER INTERACTION	35	24	1
13BJ1D0504	H4002	INFORMATION SECURITY	36	24	1
13BJ1D0506	H0501	DATA WAREHOUSING AND DATA MINING	36	29	1
13BJ1D0506	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	36	24	1
13BJ1D0506	H0503	SOTWARE ENGINEERING	36	30	1
13BJ1D0506	H0505	ADVANCED COMPUTER ARCHITECTURE ELECTIVE-	36	32	1
13BJ1D0506	H0507	HUMAN COMPUTER INTERACTION	36	25	1
13BJ1D0507	H0501	DATA WAREHOUSING AND DATA MINING	36	32	1
13BJ1D0507	H0507	HUMAN COMPUTER INTERACTION	36	33	1
13BJ1D0509	H0501	DATA WAREHOUSING AND DATA MINING	35	26	1
13BJ1D0509	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	37	24	1
13BJ1D0509	H0507	HUMAN COMPUTER INTERACTION	37	29	1
13BJ1D0509	H4002	INFORMATION SECURITY	37	29	1

Htno	Subcode	Subname	Internal	External	credits
13BJ1D0516	H0501	DATA WAREHOUSING AND DATA MINING	37	34	1
13BJ1D0517	H0501	DATA WAREHOUSING AND DATA MINING	36	34	1
13BJ1D0517	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	36	24	1
13BJ1D0517	H0503	SOTWARE ENGINEERING	36	30	1
13BJ1D0517	H0505	ADVANCED COMPUTER ARCHITECTURE ELECTIVE-	36	33	1
13BJ1D0517	H0507	HUMAN COMPUTER INTERACTION	37	24	1
13BJ1D0517	H4002	INFORMATION SECURITY	36	37	1
13BJ1D4305	H4301	SWITCHED MODE POWER CONVERSION	35	27	1
13BJ1D4305	H4303	DIGITAL CONTROLLERS	35	25	1
13BJ1D4306	H4301	SWITCHED MODE POWER CONVERSION	35	17	0
13BJ1D4306	H4303	DIGITAL CONTROLLERS	35	19	0
13BJ1D4310	H4303	DIGITAL CONTROLLERS	34	25	1
13BJ1D4312	H4303	DIGITAL CONTROLLERS	34	28	1
13BJ1D4318	H4301	SWITCHED MODE POWER CONVERSION	34	30	1
13BJ1D5502	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	35	17	0
13BJ1D5507	H5501	HARDWARE SOFTWARE CO-DESIGN	36	10	0
13BJ1D5507	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	35	19	0
13BJ1D5511	H4508	INTERNET PROTOCOLS	35	25	1
13BJ1D5511	H5501	HARDWARE SOFTWARE CO-DESIGN	35	19	0
13BJ1D5511	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	24	1
13BJ1D5511	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	35	26	1
13BJ1D5511	H6806	SYSTEM ON CHIP DESIGN	36	24	1
13BJ1D5512	H5501	HARDWARE SOFTWARE CO-DESIGN	35	15	0
13BJ1D5512	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	34	32	1
13BJ1D5512	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	35	19	0
13BJ1D5512	H6806	SYSTEM ON CHIP DESIGN	34	18	0
13BJ1D5513	H4508	INTERNET PROTOCOLS	35	24	1
13BJ1D5513	H5501	HARDWARE SOFTWARE CO-DESIGN	36	14	0
13BJ1D5513	H5502	EMBEDDED NETWORKING	36	30	1
13BJ1D5513	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	35	28	1
13BJ1D5513	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	36	30	1
13BJ1D5514	H5501	HARDWARE SOFTWARE CO-DESIGN	35	17	0
13BJ1D5514	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	35	24	1
13BJ1D5515	H4508	INTERNET PROTOCOLS	35	20	0
13BJ1D5515	H5501	HARDWARE SOFTWARE CO-DESIGN	36	3	0
13BJ1D5515	H5502	EMBEDDED NETWORKING	35	0	0
13BJ1D5515	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	35	13	0
13BJ1D5515	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	35	18	0
13BJ1D5515	H6806	SYSTEM ON CHIP DESIGN	35	5	0
13BJ1D5801	H0501	DATA WAREHOUSING AND DATA MINING	31	29	1
13BJ1D5801	H0507	HUMAN COMPUTER INTERACTION	31	36	1
13BJ1D5801	H2508	CLOUD COMPUTING	32	28	1
13BJ1D5801	H4002	INFORMATION SECURITY	31	31	1
13BJ1D5801	H5801	COMPUTER NETWORKS	32	27	1
13BJ1D5801	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	33	30	1
13BJ1D5801	H5806	CSE LAB-2	32	53	1
13BJ1D5803	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	36	29	1
13BJ1D5811	H0501	DATA WAREHOUSING AND DATA MINING	36	27	1
13BJ1D5811	H0507	HUMAN COMPUTER INTERACTION	35	30	1
13BJ1D5811	H2508	CLOUD COMPUTING	35	15	0
13BJ1D5811	H4002	INFORMATION SECURITY	35	36	1

Htno	Subcode	Subname	Internal	External	credits
13BJ1D5811	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	36	28	1
13BJ1D5814	H0501	DATA WAREHOUSING AND DATA MINING	35	28	1
13BJ1D5814	H0507	HUMAN COMPUTER INTERACTION	34	28	1
13BJ1D5814	H5801	COMPUTER NETWORKS	33	15	0
13BJ1D5814	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	34	5	0
13BJ1D5817	H0501	DATA WAREHOUSING AND DATA MINING	37	24	1
13BJ1D5817	H0507	HUMAN COMPUTER INTERACTION	36	31	1
13BJ1D5817	H2508	CLOUD COMPUTING	36	24	1
13BJ1D5817	H5801	COMPUTER NETWORKS	36	24	1
14BJ1D0402	H0401	SIMULATION MODELING OF MANUFACTURING SYS	38	12	0
14BJ1D0402	H0402	COMPUTER GRAPHICS	39	24	1
14BJ1D0402	H0404	CONCURRENT ENGINEERING ELECTIVE-III	37	30	1
14BJ1D0402	H0408	INTELLIGENT MANUFACTURING SYSTEMS	39	24	1
14BJ1D0402	H0410	MODELING AND ANALYSIS OF MANUFACTURING P	39	57	1
14BJ1D0402	H1501	OPTIMIZATION AND RELIABILITY	38	-1	0
14BJ1D0402	H2103	FINITE ELEMENT METHOD	38	0	0
14BJ1D0403	H0401	SIMULATION MODELING OF MANUFACTURING SYS	38	24	1
14BJ1D0403	H0402	COMPUTER GRAPHICS	37	15	0
14BJ1D0403	H0404	CONCURRENT ENGINEERING ELECTIVE-III	39	33	1
14BJ1D0403	H0408	INTELLIGENT MANUFACTURING SYSTEMS	37	29	1
14BJ1D0403	H0410	MODELING AND ANALYSIS OF MANUFACTURING P	36	54	1
14BJ1D0403	H1501	OPTIMIZATION AND RELIABILITY	38	24	1
14BJ1D0403	H2103	FINITE ELEMENT METHOD	37	27	1
14BJ1D0404	H0401	SIMULATION MODELING OF MANUFACTURING SYS	37	12	0
14BJ1D0404	H0402	COMPUTER GRAPHICS	38	24	1
14BJ1D0404	H0404	CONCURRENT ENGINEERING ELECTIVE-III	39	31	1
14BJ1D0404	H0408	INTELLIGENT MANUFACTURING SYSTEMS	38	24	1
14BJ1D0404	H0410	MODELING AND ANALYSIS OF MANUFACTURING P	38	57	1
14BJ1D0404	H1501	OPTIMIZATION AND RELIABILITY	39	24	1
14BJ1D0404	H2103	FINITE ELEMENT METHOD	39	28	1
14BJ1D0405	H0401	SIMULATION MODELING OF MANUFACTURING SYS	38	24	1
14BJ1D0405	H0402	COMPUTER GRAPHICS	37	29	1
14BJ1D0405	H0404	CONCURRENT ENGINEERING ELECTIVE-III	37	27	1
14BJ1D0405	H0408	INTELLIGENT MANUFACTURING SYSTEMS	37	31	1
14BJ1D0405	H0410	MODELING AND ANALYSIS OF MANUFACTURING P	39	59	1
14BJ1D0405	H1501	OPTIMIZATION AND RELIABILITY	38	25	1
14BJ1D0405	H2103	FINITE ELEMENT METHOD	38	33	1
14BJ1D0406	H0401	SIMULATION MODELING OF MANUFACTURING SYS	36	9	0
14BJ1D0406	H0402	COMPUTER GRAPHICS	36	28	1
14BJ1D0406	H0404	CONCURRENT ENGINEERING ELECTIVE-III	35	33	1
14BJ1D0406	H0408	INTELLIGENT MANUFACTURING SYSTEMS	36	29	1
14BJ1D0406	H0410	MODELING AND ANALYSIS OF MANUFACTURING P	38	55	1
14BJ1D0406	H1501	OPTIMIZATION AND RELIABILITY	35	24	1
14BJ1D0406	H2103	FINITE ELEMENT METHOD	36	25	1
14BJ1D0407	H0401	SIMULATION MODELING OF MANUFACTURING SYS	38	10	0
14BJ1D0407	H0402	COMPUTER GRAPHICS	37	24	1
14BJ1D0407	H0404	CONCURRENT ENGINEERING ELECTIVE-III	39	24	1
14BJ1D0407	H0408	INTELLIGENT MANUFACTURING SYSTEMS	37	17	0
14BJ1D0407	H0410	MODELING AND ANALYSIS OF MANUFACTURING P	36	56	1
14BJ1D0407	H1501	OPTIMIZATION AND RELIABILITY	37	26	1
14BJ1D0407	H2103	FINITE ELEMENT METHOD	38	29	1

Htno	Subcode	Subname	Internal	External	credits
14BJ1D0408	H0401	SIMULATION MODELING OF MANUFACTURING SYS	37	24	1
14BJ1D0408	H0402	COMPUTER GRAPHICS	37	24	1
14BJ1D0408	H0404	CONCURRENT ENGINEERING ELECTIVE-III	38	24	1
14BJ1D0408	H0408	INTELLIGENT MANUFACTURING SYSTEMS	37	18	0
14BJ1D0408	H0410	MODELING AND ANALYSIS OF MANUFACTURING P	39	55	1
14BJ1D0408	H1501	OPTIMIZATION AND RELIABILITY	37	-1	0
14BJ1D0408	H2103	FINITE ELEMENT METHOD	39	2	0
14BJ1D0409	H0401	SIMULATION MODELING OF MANUFACTURING SYS	39	11	0
14BJ1D0409	H0402	COMPUTER GRAPHICS	38	24	1
14BJ1D0409	H0404	CONCURRENT ENGINEERING ELECTIVE-III	38	31	1
14BJ1D0409	H0408	INTELLIGENT MANUFACTURING SYSTEMS	39	26	1
14BJ1D0409	H0410	MODELING AND ANALYSIS OF MANUFACTURING P	37	57	1
14BJ1D0409	H1501	OPTIMIZATION AND RELIABILITY	35	3	0
14BJ1D0409	H2103	FINITE ELEMENT METHOD	38	18	0
14BJ1D0410	H0401	SIMULATION MODELING OF MANUFACTURING SYS	37	17	0
14BJ1D0410	H0402	COMPUTER GRAPHICS	36	29	1
14BJ1D0410	H0404	CONCURRENT ENGINEERING ELECTIVE-III	35	28	1
14BJ1D0410	H0408	INTELLIGENT MANUFACTURING SYSTEMS	36	29	1
14BJ1D0410	H0410	MODELING AND ANALYSIS OF MANUFACTURING P	38	55	1
14BJ1D0410	H1501	OPTIMIZATION AND RELIABILITY	35	35	1
14BJ1D0410	H2103	FINITE ELEMENT METHOD	36	28	1
14BJ1D0411	H0401	SIMULATION MODELING OF MANUFACTURING SYS	39	0	0
14BJ1D0411	H0402	COMPUTER GRAPHICS	38	9	0
14BJ1D0411	H0404	CONCURRENT ENGINEERING ELECTIVE-III	37	6	0
14BJ1D0411	H0408	INTELLIGENT MANUFACTURING SYSTEMS	39	26	1
14BJ1D0411	H0410	MODELING AND ANALYSIS OF MANUFACTURING P	36	56	1
14BJ1D0411	H1501	OPTIMIZATION AND RELIABILITY	36	18	0
14BJ1D0411	H2103	FINITE ELEMENT METHOD	38	5	0
14BJ1D0412	H0401	SIMULATION MODELING OF MANUFACTURING SYS	38	17	0
14BJ1D0412	H0402	COMPUTER GRAPHICS	39	11	0
14BJ1D0412	H0404	CONCURRENT ENGINEERING ELECTIVE-III	38	31	1
14BJ1D0412	H0408	INTELLIGENT MANUFACTURING SYSTEMS	39	24	1
14BJ1D0412	H0410	MODELING AND ANALYSIS OF MANUFACTURING P	37	57	1
14BJ1D0412	H1501	OPTIMIZATION AND RELIABILITY	37	24	1
14BJ1D0412	H2103	FINITE ELEMENT METHOD	38	24	1
14BJ1D0413	H0401	SIMULATION MODELING OF MANUFACTURING SYS	36	24	1
14BJ1D0413	H0402	COMPUTER GRAPHICS	36	25	1
14BJ1D0413	H0404	CONCURRENT ENGINEERING ELECTIVE-III	35	29	1
14BJ1D0413	H0408	INTELLIGENT MANUFACTURING SYSTEMS	36	25	1
14BJ1D0413	H0410	MODELING AND ANALYSIS OF MANUFACTURING P	38	56	1
14BJ1D0413	H1501	OPTIMIZATION AND RELIABILITY	35	11	0
14BJ1D0413	H2103	FINITE ELEMENT METHOD	36	0	0
14BJ1D0501	H0501	DATA WAREHOUSING AND DATA MINING	35	24	1
14BJ1D0501	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	36	12	0
14BJ1D0501	H0503	SOTWARE ENGINEERING	37	34	1
14BJ1D0501	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	36	7	0
14BJ1D0501	H0507	HUMAN COMPUTER INTERACTION	35	33	1
14BJ1D0501	H0509	WEB TECHNOLOGIES ELECTIVE-III	36	25	1
14BJ1D0501	H0510	CS LAB-II (CONVERING THE EXPERIMENTS OF	35	55	1
14BJ1D0502	H0501	DATA WAREHOUSING AND DATA MINING	36	24	1
14BJ1D0502	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	36	15	0

Htno	Subcode	Subname	Internal	External	credits
14BJ1D0502	H0503	SOTWARE ENGINEERING	35	30	1
14BJ1D0502	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	36	14	0
14BJ1D0502	H0507	HUMAN COMPUTER INTERACTION	35	24	1
14BJ1D0502	H0509	WEB TECHNOLOGIES ELECTIVE-III	35	24	1
14BJ1D0502	H0510	CS LAB-II (CONVERING THE EXPERIMENTS OF	35	54	1
14BJ1D0503	H0501	DATA WAREHOUSING AND DATA MINING	35	27	1
14BJ1D0503	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	35	15	0
14BJ1D0503	H0503	SOTWARE ENGINEERING	35	41	1
14BJ1D0503	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	35	10	0
14BJ1D0503	H0507	HUMAN COMPUTER INTERACTION	36	31	1
14BJ1D0503	H0509	WEB TECHNOLOGIES ELECTIVE-III	35	4	0
14BJ1D0503	H0510	CS LAB-II (CONVERING THE EXPERIMENTS OF	36	55	1
14BJ1D0504	H0501	DATA WAREHOUSING AND DATA MINING	35	45	1
14BJ1D0504	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	36	27	1
14BJ1D0504	H0503	SOTWARE ENGINEERING	35	43	1
14BJ1D0504	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	35	46	1
14BJ1D0504	H0507	HUMAN COMPUTER INTERACTION	36	41	1
14BJ1D0504	H0509	WEB TECHNOLOGIES ELECTIVE-III	36	40	1
14BJ1D0504	H0510	CS LAB-II (CONVERING THE EXPERIMENTS OF	34	54	1
14BJ1D0505	H0501	DATA WAREHOUSING AND DATA MINING	37	24	1
14BJ1D0505	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	37	17	0
14BJ1D0505	H0503	SOTWARE ENGINEERING	37	35	1
14BJ1D0505	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	38	31	1
14BJ1D0505	H0507	HUMAN COMPUTER INTERACTION	38	25	1
14BJ1D0505	H0509	WEB TECHNOLOGIES ELECTIVE-III	37	12	0
14BJ1D0505	H0510	CS LAB-II (CONVERING THE EXPERIMENTS OF	37	56	1
14BJ1D0506	H0501	DATA WAREHOUSING AND DATA MINING	36	27	1
14BJ1D0506	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	35	12	0
14BJ1D0506	H0503	SOTWARE ENGINEERING	34	37	1
14BJ1D0506	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	35	39	1
14BJ1D0506	H0507	HUMAN COMPUTER INTERACTION	34	36	1
14BJ1D0506	H0509	WEB TECHNOLOGIES ELECTIVE-III	35	41	1
14BJ1D0506	H0510	CS LAB-II (CONVERING THE EXPERIMENTS OF	35	55	1
14BJ1D0507	H0501	DATA WAREHOUSING AND DATA MINING	37	24	1
14BJ1D0507	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	38	1	0
14BJ1D0507	H0503	SOTWARE ENGINEERING	38	41	1
14BJ1D0507	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	38	26	1
14BJ1D0507	H0507	HUMAN COMPUTER INTERACTION	38	26	1
14BJ1D0507	H0509	WEB TECHNOLOGIES ELECTIVE-III	37	7	0
14BJ1D0507	H0510	CS LAB-II (CONVERING THE EXPERIMENTS OF	37	57	1
14BJ1D0508	H0501	DATA WAREHOUSING AND DATA MINING	36	15	0
14BJ1D0508	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	35	6	0
14BJ1D0508	H0503	SOTWARE ENGINEERING	35	27	1
14BJ1D0508	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	35	0	0
14BJ1D0508	H0507	HUMAN COMPUTER INTERACTION	36	24	1
14BJ1D0508	H0509	WEB TECHNOLOGIES ELECTIVE-III	34	18	0
14BJ1D0508	H0510	CS LAB-II (CONVERING THE EXPERIMENTS OF	36	-1	0
14BJ1D0509	H0501	DATA WAREHOUSING AND DATA MINING	35	15	0
14BJ1D0509	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	36	14	0
14BJ1D0509	H0503	SOTWARE ENGINEERING	35	34	1
14BJ1D0509	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	36	24	1

Htno	Subcode	Subname	Internal	External	credits
14BJ1D0509	H0507	HUMAN COMPUTER INTERACTION	36	24	1
14BJ1D0509	H0509	WEB TECHNOLOGIES ELECTIVE-III	35	36	1
14BJ1D0509	H0510	CS LAB-II (CONVERING THE EXPERIMENTS OF	34	56	1
14BJ1D0510	H0501	DATA WAREHOUSING AND DATA MINING	38	24	1
14BJ1D0510	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	38	14	0
14BJ1D0510	H0503	SOTWARE ENGINEERING	37	40	1
14BJ1D0510	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	37	26	1
14BJ1D0510	H0507	HUMAN COMPUTER INTERACTION	37	25	1
14BJ1D0510	H0509	WEB TECHNOLOGIES ELECTIVE-III	38	33	1
14BJ1D0510	H0510	CS LAB-II (CONVERING THE EXPERIMENTS OF	36	57	1
14BJ1D0511	H0501	DATA WAREHOUSING AND DATA MINING	35	29	1
14BJ1D0511	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	36	24	1
14BJ1D0511	H0503	SOTWARE ENGINEERING	35	33	1
14BJ1D0511	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	36	25	1
14BJ1D0511	H0507	HUMAN COMPUTER INTERACTION	36	29	1
14BJ1D0511	H0509	WEB TECHNOLOGIES ELECTIVE-III	35	30	1
14BJ1D0511	H0510	CS LAB-II (CONVERING THE EXPERIMENTS OF	34	55	1
14BJ1D0512	H0501	DATA WAREHOUSING AND DATA MINING	38	25	1
14BJ1D0512	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	38	32	1
14BJ1D0512	H0503	SOTWARE ENGINEERING	36	40	1
14BJ1D0512	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	36	39	1
14BJ1D0512	H0507	HUMAN COMPUTER INTERACTION	38	25	1
14BJ1D0512	H0509	WEB TECHNOLOGIES ELECTIVE-III	37	24	1
14BJ1D0512	H0510	CS LAB-II (CONVERING THE EXPERIMENTS OF	37	57	1
14BJ1D0513	H0501	DATA WAREHOUSING AND DATA MINING	34	24	1
14BJ1D0513	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	34	5	0
14BJ1D0513	H0503	SOTWARE ENGINEERING	36	29	1
14BJ1D0513	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	35	24	1
14BJ1D0513	H0507	HUMAN COMPUTER INTERACTION	34	27	1
14BJ1D0513	H0509	WEB TECHNOLOGIES ELECTIVE-III	35	18	0
14BJ1D0513	H0510	CS LAB-II (CONVERING THE EXPERIMENTS OF	35	54	1
14BJ1D0514	H0501	DATA WAREHOUSING AND DATA MINING	35	37	1
14BJ1D0514	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	35	24	1
14BJ1D0514	H0503	SOTWARE ENGINEERING	36	37	1
14BJ1D0514	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	36	30	1
14BJ1D0514	H0507	HUMAN COMPUTER INTERACTION	36	36	1
14BJ1D0514	H0509	WEB TECHNOLOGIES ELECTIVE-III	36	33	1
14BJ1D0514	H0510	CS LAB-II (CONVERING THE EXPERIMENTS OF	34	55	1
14BJ1D0515	H0501	DATA WAREHOUSING AND DATA MINING	37	-1	0
14BJ1D0515	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	36	-1	0
14BJ1D0515	H0503	SOTWARE ENGINEERING	36	-1	0
14BJ1D0515	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	37	-1	0
14BJ1D0515	H0507	HUMAN COMPUTER INTERACTION	36	-1	0
14BJ1D0515	H0509	WEB TECHNOLOGIES ELECTIVE-III	36	-1	0
14BJ1D0515	H0510	CS LAB-II (CONVERING THE EXPERIMENTS OF	35	-1	0
14BJ1D0516	H0501	DATA WAREHOUSING AND DATA MINING	36	32	1
14BJ1D0516	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	35	5	0
14BJ1D0516	H0503	SOTWARE ENGINEERING	36	34	1
14BJ1D0516	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	36	24	1
14BJ1D0516	H0507	HUMAN COMPUTER INTERACTION	35	33	1
14BJ1D0516	H0509	WEB TECHNOLOGIES ELECTIVE-III	35	31	1

Htno	Subcode	Subname	Internal	External	credits
14BJ1D0516	H0510	CS LAB-II (CONVERGING THE EXPERIMENTS OF	35	55	1
14BJ1D0517	H0501	DATA WAREHOUSING AND DATA MINING	36	-1	0
14BJ1D0517	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	36	6	0
14BJ1D0517	H0503	SOTWARE ENGINEERING	36	-1	0
14BJ1D0517	H0504	AUTOMATA THEORY AND COMPILER DESIGN ELE	34	-1	0
14BJ1D0517	H0507	HUMAN COMPUTER INTERACTION	35	-1	0
14BJ1D0517	H0509	WEB TECHNOLOGIES ELECTIVE-III	35	-1	0
14BJ1D0517	H0510	CS LAB-II (CONVERGING THE EXPERIMENTS OF	34	-1	0
14BJ1D2101	H2101	FUELSCOMBUSTION & ENVIRONMENT	37	4	0
14BJ1D2101	H2102	ENERGY MANAGEMENT	37	24	1
14BJ1D2101	H2103	FINITE ELEMENT METHOD	37	8	0
14BJ1D2101	H2104	COMPUTATIONAL FLUID DYNAMICS	36	11	0
14BJ1D2101	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	36	0	0
14BJ1D2101	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	35	24	1
14BJ1D2101	H2113	THERMAL SYSTEMS DESIGN LAB	38	56	1
14BJ1D2102	H2101	FUELSCOMBUSTION & ENVIRONMENT	39	14	0
14BJ1D2102	H2102	ENERGY MANAGEMENT	39	29	1
14BJ1D2102	H2103	FINITE ELEMENT METHOD	38	42	1
14BJ1D2102	H2104	COMPUTATIONAL FLUID DYNAMICS	39	40	1
14BJ1D2102	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	39	26	1
14BJ1D2102	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	38	32	1
14BJ1D2102	H2113	THERMAL SYSTEMS DESIGN LAB	39	57	1
14BJ1D2103	H2101	FUELSCOMBUSTION & ENVIRONMENT	39	30	1
14BJ1D2103	H2102	ENERGY MANAGEMENT	37	16	0
14BJ1D2103	H2103	FINITE ELEMENT METHOD	37	6	0
14BJ1D2103	H2104	COMPUTATIONAL FLUID DYNAMICS	37	30	1
14BJ1D2103	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	39	3	0
14BJ1D2103	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	38	28	1
14BJ1D2103	H2113	THERMAL SYSTEMS DESIGN LAB	36	55	1
14BJ1D2105	H2101	FUELSCOMBUSTION & ENVIRONMENT	37	36	1
14BJ1D2105	H2102	ENERGY MANAGEMENT	37	26	1
14BJ1D2105	H2103	FINITE ELEMENT METHOD	38	14	0
14BJ1D2105	H2104	COMPUTATIONAL FLUID DYNAMICS	36	33	1
14BJ1D2105	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	37	29	1
14BJ1D2105	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	38	28	1
14BJ1D2105	H2113	THERMAL SYSTEMS DESIGN LAB	36	56	1
14BJ1D2106	H2101	FUELSCOMBUSTION & ENVIRONMENT	36	2	0
14BJ1D2106	H2102	ENERGY MANAGEMENT	36	8	0
14BJ1D2106	H2103	FINITE ELEMENT METHOD	36	0	0
14BJ1D2106	H2104	COMPUTATIONAL FLUID DYNAMICS	36	24	1
14BJ1D2106	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	35	0	0
14BJ1D2106	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	35	24	1
14BJ1D2106	H2113	THERMAL SYSTEMS DESIGN LAB	38	55	1
14BJ1D2107	H2101	FUELSCOMBUSTION & ENVIRONMENT	37	18	0
14BJ1D2107	H2102	ENERGY MANAGEMENT	37	24	1
14BJ1D2107	H2103	FINITE ELEMENT METHOD	38	24	1
14BJ1D2107	H2104	COMPUTATIONAL FLUID DYNAMICS	37	12	0
14BJ1D2107	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	39	21	0
14BJ1D2107	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	37	26	1
14BJ1D2107	H2113	THERMAL SYSTEMS DESIGN LAB	36	56	1
14BJ1D2108	H2101	FUELSCOMBUSTION & ENVIRONMENT	37	16	0

Htno	Subcode	Subname	Internal	External	credits
14BJ1D2108	H2102	ENERGY MANAGEMENT	37	24	1
14BJ1D2108	H2103	FINITE ELEMENT METHOD	39	0	0
14BJ1D2108	H2104	COMPUTATIONAL FLUID DYNAMICS	38	26	1
14BJ1D2108	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	38	24	1
14BJ1D2108	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	37	30	1
14BJ1D2108	H2113	THERMAL SYSTEMS DESIGN LAB	39	56	1
14BJ1D2109	H2101	FUELSCOMBUSTION & ENVIRONMENT	39	10	0
14BJ1D2109	H2102	ENERGY MANAGEMENT	39	19	0
14BJ1D2109	H2103	FINITE ELEMENT METHOD	38	36	1
14BJ1D2109	H2104	COMPUTATIONAL FLUID DYNAMICS	37	29	1
14BJ1D2109	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	38	24	1
14BJ1D2109	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	35	26	1
14BJ1D2109	H2113	THERMAL SYSTEMS DESIGN LAB	37	57	1
14BJ1D2110	H2101	FUELSCOMBUSTION & ENVIRONMENT	36	16	0
14BJ1D2110	H2102	ENERGY MANAGEMENT	36	24	1
14BJ1D2110	H2103	FINITE ELEMENT METHOD	36	24	1
14BJ1D2110	H2104	COMPUTATIONAL FLUID DYNAMICS	36	31	1
14BJ1D2110	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	35	24	1
14BJ1D2110	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	35	26	1
14BJ1D2110	H2113	THERMAL SYSTEMS DESIGN LAB	38	55	1
14BJ1D2111	H2101	FUELSCOMBUSTION & ENVIRONMENT	38	6	0
14BJ1D2111	H2102	ENERGY MANAGEMENT	39	4	0
14BJ1D2111	H2103	FINITE ELEMENT METHOD	38	4	0
14BJ1D2111	H2104	COMPUTATIONAL FLUID DYNAMICS	37	8	0
14BJ1D2111	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	37	14	0
14BJ1D2111	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	36	30	1
14BJ1D2111	H2113	THERMAL SYSTEMS DESIGN LAB	36	56	1
14BJ1D2112	H2101	FUELSCOMBUSTION & ENVIRONMENT	39	16	0
14BJ1D2112	H2102	ENERGY MANAGEMENT	39	27	1
14BJ1D2112	H2103	FINITE ELEMENT METHOD	38	27	1
14BJ1D2112	H2104	COMPUTATIONAL FLUID DYNAMICS	39	28	1
14BJ1D2112	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	39	26	1
14BJ1D2112	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	37	6	0
14BJ1D2112	H2113	THERMAL SYSTEMS DESIGN LAB	37	-1	0
14BJ1D2113	H2101	FUELSCOMBUSTION & ENVIRONMENT	36	6	0
14BJ1D2113	H2102	ENERGY MANAGEMENT	36	24	1
14BJ1D2113	H2104	COMPUTATIONAL FLUID DYNAMICS	36	35	1
14BJ1D2113	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	35	38	1
14BJ1D2113	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	35	30	1
14BJ1D2113	H2113	THERMAL SYSTEMS DESIGN LAB	38	55	1
14BJ1D2114	H2101	FUELSCOMBUSTION & ENVIRONMENT	38	14	0
14BJ1D2114	H2102	ENERGY MANAGEMENT	39	25	1
14BJ1D2114	H2103	FINITE ELEMENT METHOD	39	16	0
14BJ1D2114	H2104	COMPUTATIONAL FLUID DYNAMICS	38	28	1
14BJ1D2114	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	38	24	1
14BJ1D2114	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	38	24	1
14BJ1D2114	H2113	THERMAL SYSTEMS DESIGN LAB	39	56	1
14BJ1D2115	H2101	FUELSCOMBUSTION & ENVIRONMENT	37	16	0
14BJ1D2115	H2102	ENERGY MANAGEMENT	38	10	0
14BJ1D2115	H2103	FINITE ELEMENT METHOD	37	39	1
14BJ1D2115	H2104	COMPUTATIONAL FLUID DYNAMICS	38	30	1

Htno	Subcode	Subname	Internal	External	credits
14BJ1D2115	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	39	33	1
14BJ1D2115	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	37	28	1
14BJ1D2115	H2113	THERMAL SYSTEMS DESIGN LAB	39	56	1
14BJ1D2116	H2101	FUELSCOMBUSTION & ENVIRONMENT	39	28	1
14BJ1D2116	H2102	ENERGY MANAGEMENT	37	24	1
14BJ1D2116	H2103	FINITE ELEMENT METHOD	38	30	1
14BJ1D2116	H2104	COMPUTATIONAL FLUID DYNAMICS	37	37	1
14BJ1D2116	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	38	25	1
14BJ1D2116	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	38	33	1
14BJ1D2116	H2113	THERMAL SYSTEMS DESIGN LAB	37	59	1
14BJ1D2117	H2101	FUELSCOMBUSTION & ENVIRONMENT	37	-1	0
14BJ1D2117	H2102	ENERGY MANAGEMENT	37	-1	0
14BJ1D2117	H2103	FINITE ELEMENT METHOD	38	-1	0
14BJ1D2117	H2104	COMPUTATIONAL FLUID DYNAMICS	38	-1	0
14BJ1D2117	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	40	-1	0
14BJ1D2117	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	35	-1	0
14BJ1D2117	H2113	THERMAL SYSTEMS DESIGN LAB	36	-1	0
14BJ1D2118	H2101	FUELSCOMBUSTION & ENVIRONMENT	39	12	0
14BJ1D2118	H2102	ENERGY MANAGEMENT	38	31	1
14BJ1D2118	H2103	FINITE ELEMENT METHOD	38	17	0
14BJ1D2118	H2104	COMPUTATIONAL FLUID DYNAMICS	39	40	1
14BJ1D2118	H2108	ADVANCED AUTOMOBILE ENGG ELECTIVE-III	39	28	1
14BJ1D2118	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	39	33	1
14BJ1D2118	H2113	THERMAL SYSTEMS DESIGN LAB	38	55	1
14BJ1D4301	H4301	SWITCHED MODE POWER CONVERSION	33	12	0
14BJ1D4301	H4302	ELECTRIC DRIVES-II	37	28	1
14BJ1D4301	H4303	DIGITAL CONTROLLERS	34	26	1
14BJ1D4301	H4304	CUSTOM POWER DEVICES	31	39	1
14BJ1D4301	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	32	4	0
14BJ1D4301	H4308	SPECIAL MACHINES ELECTIVE-IV	37	33	1
14BJ1D4301	H4310	POWER CONVERTERS & DRIVES LAB	38	57	1
14BJ1D4303	H4301	SWITCHED MODE POWER CONVERSION	27	-1	0
14BJ1D4303	H4302	ELECTRIC DRIVES-II	27	-1	0
14BJ1D4303	H4303	DIGITAL CONTROLLERS	28	-1	0
14BJ1D4303	H4304	CUSTOM POWER DEVICES	27	-1	0
14BJ1D4303	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	27	-1	0
14BJ1D4303	H4308	SPECIAL MACHINES ELECTIVE-IV	27	-1	0
14BJ1D4303	H4310	POWER CONVERTERS & DRIVES LAB	27	-1	0
14BJ1D4304	H4301	SWITCHED MODE POWER CONVERSION	38	10	0
14BJ1D4304	H4302	ELECTRIC DRIVES-II	37	0	0
14BJ1D4304	H4303	DIGITAL CONTROLLERS	38	6	0
14BJ1D4304	H4304	CUSTOM POWER DEVICES	39	7	0
14BJ1D4304	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	34	15	0
14BJ1D4304	H4308	SPECIAL MACHINES ELECTIVE-IV	38	24	1
14BJ1D4304	H4310	POWER CONVERTERS & DRIVES LAB	35	47	1
14BJ1D4305	H4301	SWITCHED MODE POWER CONVERSION	37	11	0
14BJ1D4305	H4302	ELECTRIC DRIVES-II	35	12	0
14BJ1D4305	H4303	DIGITAL CONTROLLERS	36	25	1
14BJ1D4305	H4304	CUSTOM POWER DEVICES	37	33	1
14BJ1D4305	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	35	7	0
14BJ1D4305	H4308	SPECIAL MACHINES ELECTIVE-IV	36	31	1

Htno	Subcode	Subname	Internal	External	credits
14BJ1D4305	H4310	POWER CONVERTERS & DRIVES LAB	38	58	1
14BJ1D4306	H4301	SWITCHED MODE POWER CONVERSION	36	27	1
14BJ1D4306	H4302	ELECTRIC DRIVES-II	36	40	1
14BJ1D4306	H4303	DIGITAL CONTROLLERS	31	32	1
14BJ1D4306	H4304	CUSTOM POWER DEVICES	31	35	1
14BJ1D4306	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	36	24	1
14BJ1D4306	H4308	SPECIAL MACHINES ELECTIVE-IV	38	30	1
14BJ1D4306	H4310	POWER CONVERTERS & DRIVES LAB	36	53	1
14BJ1D4308	H4301	SWITCHED MODE POWER CONVERSION	36	17	0
14BJ1D4308	H4302	ELECTRIC DRIVES-II	36	0	0
14BJ1D4308	H4303	DIGITAL CONTROLLERS	35	13	0
14BJ1D4308	H4304	CUSTOM POWER DEVICES	35	26	1
14BJ1D4308	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	34	9	0
14BJ1D4308	H4308	SPECIAL MACHINES ELECTIVE-IV	36	15	0
14BJ1D4308	H4310	POWER CONVERTERS & DRIVES LAB	35	46	1
14BJ1D4309	H4301	SWITCHED MODE POWER CONVERSION	37	27	1
14BJ1D4309	H4302	ELECTRIC DRIVES-II	33	37	1
14BJ1D4309	H4303	DIGITAL CONTROLLERS	37	31	1
14BJ1D4309	H4304	CUSTOM POWER DEVICES	34	28	1
14BJ1D4309	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	36	24	1
14BJ1D4309	H4308	SPECIAL MACHINES ELECTIVE-IV	38	28	1
14BJ1D4309	H4310	POWER CONVERTERS & DRIVES LAB	39	58	1
14BJ1D4310	H4301	SWITCHED MODE POWER CONVERSION	35	12	0
14BJ1D4310	H4302	ELECTRIC DRIVES-II	30	2	0
14BJ1D4310	H4303	DIGITAL CONTROLLERS	32	2	0
14BJ1D4310	H4304	CUSTOM POWER DEVICES	33	0	0
14BJ1D4310	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	30	1	0
14BJ1D4310	H4308	SPECIAL MACHINES ELECTIVE-IV	32	32	1
14BJ1D4310	H4310	POWER CONVERTERS & DRIVES LAB	27	44	1
14BJ1D4311	H4301	SWITCHED MODE POWER CONVERSION	38	27	1
14BJ1D4311	H4302	ELECTRIC DRIVES-II	38	3	0
14BJ1D4311	H4303	DIGITAL CONTROLLERS	38	8	0
14BJ1D4311	H4304	CUSTOM POWER DEVICES	37	0	0
14BJ1D4311	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	33	14	0
14BJ1D4311	H4308	SPECIAL MACHINES ELECTIVE-IV	37	27	1
14BJ1D4311	H4310	POWER CONVERTERS & DRIVES LAB	39	50	1
14BJ1D4312	H4301	SWITCHED MODE POWER CONVERSION	35	4	0
14BJ1D4312	H4302	ELECTRIC DRIVES-II	35	27	1
14BJ1D4312	H4303	DIGITAL CONTROLLERS	32	24	1
14BJ1D4312	H4304	CUSTOM POWER DEVICES	27	10	0
14BJ1D4312	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	27	7	0
14BJ1D4312	H4308	SPECIAL MACHINES ELECTIVE-IV	27	12	0
14BJ1D4312	H4310	POWER CONVERTERS & DRIVES LAB	38	57	1
14BJ1D4313	H4301	SWITCHED MODE POWER CONVERSION	35	16	0
14BJ1D4313	H4302	ELECTRIC DRIVES-II	32	1	0
14BJ1D4313	H4303	DIGITAL CONTROLLERS	33	14	0
14BJ1D4313	H4304	CUSTOM POWER DEVICES	33	4	0
14BJ1D4313	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	33	8	0
14BJ1D4313	H4308	SPECIAL MACHINES ELECTIVE-IV	35	26	1
14BJ1D4313	H4310	POWER CONVERTERS & DRIVES LAB	35	56	1
14BJ1D4314	H4301	SWITCHED MODE POWER CONVERSION	38	29	1

Htno	Subcode	Subname	Internal	External	credits
14BJ1D4314	H4302	ELECTRIC DRIVES-II	36	45	1
14BJ1D4314	H4303	DIGITAL CONTROLLERS	37	33	1
14BJ1D4314	H4304	CUSTOM POWER DEVICES	40	35	1
14BJ1D4314	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	36	26	1
14BJ1D4314	H4308	SPECIAL MACHINES ELECTIVE-IV	38	26	1
14BJ1D4314	H4310	POWER CONVERTERS & DRIVES LAB	39	53	1
14BJ1D4315	H4301	SWITCHED MODE POWER CONVERSION	33	9	0
14BJ1D4315	H4302	ELECTRIC DRIVES-II	36	26	1
14BJ1D4315	H4303	DIGITAL CONTROLLERS	35	24	1
14BJ1D4315	H4304	CUSTOM POWER DEVICES	38	25	1
14BJ1D4315	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	34	24	1
14BJ1D4315	H4308	SPECIAL MACHINES ELECTIVE-IV	37	24	1
14BJ1D4315	H4310	POWER CONVERTERS & DRIVES LAB	35	56	1
14BJ1D4317	H4301	SWITCHED MODE POWER CONVERSION	34	3	0
14BJ1D4317	H4302	ELECTRIC DRIVES-II	30	15	0
14BJ1D4317	H4303	DIGITAL CONTROLLERS	33	25	1
14BJ1D4317	H4304	CUSTOM POWER DEVICES	33	24	1
14BJ1D4317	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	31	15	0
14BJ1D4317	H4308	SPECIAL MACHINES ELECTIVE-IV	35	26	1
14BJ1D4317	H4310	POWER CONVERTERS & DRIVES LAB	37	57	1
14BJ1D5501	H4508	INTERNET PROTOCOLS	37	28	1
14BJ1D5501	H5501	HARDWARE SOFTWARE CO-DESIGN	36	26	1
14BJ1D5501	H5502	EMBEDDED NETWORKING	37	29	1
14BJ1D5501	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	35	29	1
14BJ1D5501	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	36	25	1
14BJ1D5501	H6806	SYSTEM ON CHIP DESIGN	35	15	0
14BJ1D5501	H6811	EMBEDDED SYSTEMS LAB	39	56	1
14BJ1D5502	H4508	INTERNET PROTOCOLS	36	34	1
14BJ1D5502	H5501	HARDWARE SOFTWARE CO-DESIGN	37	27	1
14BJ1D5502	H5502	EMBEDDED NETWORKING	36	27	1
14BJ1D5502	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	39	26	1
14BJ1D5502	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	39	29	1
14BJ1D5502	H6806	SYSTEM ON CHIP DESIGN	37	25	1
14BJ1D5502	H6811	EMBEDDED SYSTEMS LAB	38	57	1
14BJ1D5503	H4508	INTERNET PROTOCOLS	38	36	1
14BJ1D5503	H5501	HARDWARE SOFTWARE CO-DESIGN	37	24	1
14BJ1D5503	H5502	EMBEDDED NETWORKING	37	35	1
14BJ1D5503	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	38	30	1
14BJ1D5503	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	38	44	1
14BJ1D5503	H6806	SYSTEM ON CHIP DESIGN	38	24	1
14BJ1D5503	H6811	EMBEDDED SYSTEMS LAB	38	56	1
14BJ1D5504	H4508	INTERNET PROTOCOLS	37	26	1
14BJ1D5504	H5501	HARDWARE SOFTWARE CO-DESIGN	36	24	1
14BJ1D5504	H5502	EMBEDDED NETWORKING	35	26	1
14BJ1D5504	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	39	24	1
14BJ1D5504	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	37	24	1
14BJ1D5504	H6806	SYSTEM ON CHIP DESIGN	38	8	0
14BJ1D5504	H6811	EMBEDDED SYSTEMS LAB	37	54	1
14BJ1D5505	H4508	INTERNET PROTOCOLS	38	30	1
14BJ1D5505	H5501	HARDWARE SOFTWARE CO-DESIGN	37	27	1
14BJ1D5505	H5502	EMBEDDED NETWORKING	38	34	1

Htno	Subcode	Subname	Internal	External	credits
14BJ1D5505	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	29	1
14BJ1D5505	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	34	24	1
14BJ1D5505	H6806	SYSTEM ON CHIP DESIGN	37	9	0
14BJ1D5505	H6811	EMBEDDED SYSTEMS LAB	37	55	1
14BJ1D5506	H4508	INTERNET PROTOCOLS	37	29	1
14BJ1D5506	H5501	HARDWARE SOFTWARE CO-DESIGN	37	24	1
14BJ1D5506	H5502	EMBEDDED NETWORKING	36	32	1
14BJ1D5506	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	24	1
14BJ1D5506	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	35	26	1
14BJ1D5506	H6806	SYSTEM ON CHIP DESIGN	36	13	0
14BJ1D5506	H6811	EMBEDDED SYSTEMS LAB	38	52	1
14BJ1D5508	H4508	INTERNET PROTOCOLS	35	33	1
14BJ1D5508	H5501	HARDWARE SOFTWARE CO-DESIGN	36	24	1
14BJ1D5508	H5502	EMBEDDED NETWORKING	37	31	1
14BJ1D5508	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	34	31	1
14BJ1D5508	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	37	33	1
14BJ1D5508	H6806	SYSTEM ON CHIP DESIGN	37	31	1
14BJ1D5508	H6811	EMBEDDED SYSTEMS LAB	35	45	1
14BJ1D5509	H4508	INTERNET PROTOCOLS	38	27	1
14BJ1D5509	H5501	HARDWARE SOFTWARE CO-DESIGN	36	16	0
14BJ1D5509	H5502	EMBEDDED NETWORKING	37	25	1
14BJ1D5509	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	19	0
14BJ1D5509	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	34	18	0
14BJ1D5509	H6806	SYSTEM ON CHIP DESIGN	36	7	0
14BJ1D5509	H6811	EMBEDDED SYSTEMS LAB	39	57	1
14BJ1D5510	H4508	INTERNET PROTOCOLS	38	-1	0
14BJ1D5510	H5501	HARDWARE SOFTWARE CO-DESIGN	35	-1	0
14BJ1D5510	H5502	EMBEDDED NETWORKING	34	-1	0
14BJ1D5510	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	34	-1	0
14BJ1D5510	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	35	-1	0
14BJ1D5510	H6806	SYSTEM ON CHIP DESIGN	36	-1	0
14BJ1D5510	H6811	EMBEDDED SYSTEMS LAB	37	-1	0
14BJ1D5511	H4508	INTERNET PROTOCOLS	37	24	1
14BJ1D5511	H5501	HARDWARE SOFTWARE CO-DESIGN	37	13	0
14BJ1D5511	H5502	EMBEDDED NETWORKING	36	35	1
14BJ1D5511	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	37	26	1
14BJ1D5511	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	36	16	0
14BJ1D5511	H6806	SYSTEM ON CHIP DESIGN	38	14	0
14BJ1D5511	H6811	EMBEDDED SYSTEMS LAB	36	48	1
14BJ1D5512	H4508	INTERNET PROTOCOLS	37	-1	0
14BJ1D5512	H5501	HARDWARE SOFTWARE CO-DESIGN	37	17	0
14BJ1D5512	H5502	EMBEDDED NETWORKING	38	7	0
14BJ1D5512	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	0	0
14BJ1D5512	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	37	15	0
14BJ1D5512	H6806	SYSTEM ON CHIP DESIGN	35	5	0
14BJ1D5512	H6811	EMBEDDED SYSTEMS LAB	36	48	1
14BJ1D5514	H4508	INTERNET PROTOCOLS	35	-1	0
14BJ1D5514	H5501	HARDWARE SOFTWARE CO-DESIGN	36	10	0
14BJ1D5514	H5502	EMBEDDED NETWORKING	35	9	0
14BJ1D5514	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	11	0
14BJ1D5514	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	36	-1	0

Htno	Subcode	Subname	Internal	External	credits
14BJ1D5514	H6806	SYSTEM ON CHIP DESIGN	35	-1	0
14BJ1D5514	H6811	EMBEDDED SYSTEMS LAB	35	46	1
14BJ1D5515	H4508	INTERNET PROTOCOLS	36	24	1
14BJ1D5515	H5501	HARDWARE SOFTWARE CO-DESIGN	37	19	0
14BJ1D5515	H5502	EMBEDDED NETWORKING	37	15	0
14BJ1D5515	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	38	19	0
14BJ1D5515	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	38	24	1
14BJ1D5515	H6806	SYSTEM ON CHIP DESIGN	37	24	1
14BJ1D5515	H6811	EMBEDDED SYSTEMS LAB	37	46	1
14BJ1D5516	H4508	INTERNET PROTOCOLS	38	27	1
14BJ1D5516	H5501	HARDWARE SOFTWARE CO-DESIGN	37	24	1
14BJ1D5516	H5502	EMBEDDED NETWORKING	36	29	1
14BJ1D5516	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	24	1
14BJ1D5516	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	36	24	1
14BJ1D5516	H6806	SYSTEM ON CHIP DESIGN	38	30	1
14BJ1D5516	H6811	EMBEDDED SYSTEMS LAB	37	54	1
14BJ1D5517	H4508	INTERNET PROTOCOLS	37	29	1
14BJ1D5517	H5501	HARDWARE SOFTWARE CO-DESIGN	36	13	0
14BJ1D5517	H5502	EMBEDDED NETWORKING	37	33	1
14BJ1D5517	H5503	CPLD AND FPGA ARCHITECTURES AND APPLICAT	37	26	1
14BJ1D5517	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	34	17	0
14BJ1D5517	H6806	SYSTEM ON CHIP DESIGN	34	29	1
14BJ1D5517	H6811	EMBEDDED SYSTEMS LAB	37	52	1
14BJ1D5801	H0501	DATA WAREHOUSING AND DATA MINING	37	36	1
14BJ1D5801	H4001	ADVANCED UNIX PROGRAMMING	38	37	1
14BJ1D5801	H4002	INFORMATION SECURITY	37	32	1
14BJ1D5801	H5801	COMPUTER NETWORKS	38	29	1
14BJ1D5801	H5803	COMPILER DESIGN ELECTIVE-I	38	27	1
14BJ1D5801	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	38	32	1
14BJ1D5801	H5806	CSE LAB-2	37	54	1
14BJ1D5802	H0501	DATA WAREHOUSING AND DATA MINING	38	25	1
14BJ1D5802	H4001	ADVANCED UNIX PROGRAMMING	38	15	0
14BJ1D5802	H4002	INFORMATION SECURITY	36	24	1
14BJ1D5802	H5801	COMPUTER NETWORKS	36	24	1
14BJ1D5802	H5803	COMPILER DESIGN ELECTIVE-I	37	10	0
14BJ1D5802	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	36	24	1
14BJ1D5802	H5806	CSE LAB-2	38	54	1
14BJ1D5803	H0501	DATA WAREHOUSING AND DATA MINING	36	30	1
14BJ1D5803	H4001	ADVANCED UNIX PROGRAMMING	35	28	1
14BJ1D5803	H4002	INFORMATION SECURITY	35	30	1
14BJ1D5803	H5801	COMPUTER NETWORKS	35	24	1
14BJ1D5803	H5803	COMPILER DESIGN ELECTIVE-I	35	24	1
14BJ1D5803	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	35	29	1
14BJ1D5803	H5806	CSE LAB-2	35	53	1
14BJ1D5804	H0501	DATA WAREHOUSING AND DATA MINING	38	41	1
14BJ1D5804	H4001	ADVANCED UNIX PROGRAMMING	37	41	1
14BJ1D5804	H4002	INFORMATION SECURITY	37	28	1
14BJ1D5804	H5801	COMPUTER NETWORKS	37	24	1
14BJ1D5804	H5803	COMPILER DESIGN ELECTIVE-I	36	30	1
14BJ1D5804	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	37	32	1
14BJ1D5804	H5806	CSE LAB-2	37	55	1

Htno	Subcode	Subname	Internal	External	credits
14BJ1D5805	H0501	DATA WAREHOUSING AND DATA MINING	36	24	1
14BJ1D5805	H4001	ADVANCED UNIX PROGRAMMING	36	24	1
14BJ1D5805	H4002	INFORMATION SECURITY	36	27	1
14BJ1D5805	H5801	COMPUTER NETWORKS	35	30	1
14BJ1D5805	H5803	COMPILER DESIGN ELECTIVE-I	37	12	0
14BJ1D5805	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	35	24	1
14BJ1D5805	H5806	CSE LAB-2	36	54	1
14BJ1D5806	H0501	DATA WAREHOUSING AND DATA MINING	36	38	1
14BJ1D5806	H4001	ADVANCED UNIX PROGRAMMING	35	30	1
14BJ1D5806	H4002	INFORMATION SECURITY	37	24	1
14BJ1D5806	H5801	COMPUTER NETWORKS	36	31	1
14BJ1D5806	H5803	COMPILER DESIGN ELECTIVE-I	35	28	1
14BJ1D5806	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	36	33	1
14BJ1D5806	H5806	CSE LAB-2	36	55	1
14BJ1D5807	H0501	DATA WAREHOUSING AND DATA MINING	35	44	1
14BJ1D5807	H4001	ADVANCED UNIX PROGRAMMING	37	37	1
14BJ1D5807	H4002	INFORMATION SECURITY	37	31	1
14BJ1D5807	H5801	COMPUTER NETWORKS	35	38	1
14BJ1D5807	H5803	COMPILER DESIGN ELECTIVE-I	35	29	1
14BJ1D5807	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	38	32	1
14BJ1D5807	H5806	CSE LAB-2	34	56	1
14BJ1D5808	H0501	DATA WAREHOUSING AND DATA MINING	35	52	1
14BJ1D5808	H4001	ADVANCED UNIX PROGRAMMING	37	48	1
14BJ1D5808	H4002	INFORMATION SECURITY	36	43	1
14BJ1D5808	H5801	COMPUTER NETWORKS	35	36	1
14BJ1D5808	H5803	COMPILER DESIGN ELECTIVE-I	36	35	1
14BJ1D5808	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	35	32	1
14BJ1D5808	H5806	CSE LAB-2	34	56	1
14BJ1D5810	H0501	DATA WAREHOUSING AND DATA MINING	35	37	1
14BJ1D5810	H4001	ADVANCED UNIX PROGRAMMING	36	24	1
14BJ1D5810	H4002	INFORMATION SECURITY	35	24	1
14BJ1D5810	H5801	COMPUTER NETWORKS	35	24	1
14BJ1D5810	H5803	COMPILER DESIGN ELECTIVE-I	36	10	0
14BJ1D5810	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	35	29	1
14BJ1D5810	H5806	CSE LAB-2	35	53	1
14BJ1D5811	H0501	DATA WAREHOUSING AND DATA MINING	35	13	0
14BJ1D5811	H4001	ADVANCED UNIX PROGRAMMING	37	26	1
14BJ1D5811	H4002	INFORMATION SECURITY	36	16	0
14BJ1D5811	H5801	COMPUTER NETWORKS	35	26	1
14BJ1D5811	H5803	COMPILER DESIGN ELECTIVE-I	36	3	0
14BJ1D5811	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	35	12	0
14BJ1D5811	H5806	CSE LAB-2	37	-1	0
14BJ1D5813	H0501	DATA WAREHOUSING AND DATA MINING	35	1	0
14BJ1D5813	H4001	ADVANCED UNIX PROGRAMMING	35	9	0
14BJ1D5813	H4002	INFORMATION SECURITY	35	-1	0
14BJ1D5813	H5801	COMPUTER NETWORKS	34	-1	0
14BJ1D5813	H5803	COMPILER DESIGN ELECTIVE-I	35	0	0
14BJ1D5813	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	35	3	0
14BJ1D5813	H5806	CSE LAB-2	35	-1	0
14BJ1D5814	H0501	DATA WAREHOUSING AND DATA MINING	35	13	0
14BJ1D5814	H4001	ADVANCED UNIX PROGRAMMING	35	17	0

Htno	Subcode	Subname	Internal	External	credits
14BJ1D5814	H4002	INFORMATION SECURITY	35	2	0
14BJ1D5814	H5801	COMPUTER NETWORKS	36	-1	0
14BJ1D5814	H5803	COMPILER DESIGN ELECTIVE-I	35	2	0
14BJ1D5814	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	34	2	0
14BJ1D5814	H5806	CSE LAB-2	35	-1	0
14BJ1D5815	H0501	DATA WAREHOUSING AND DATA MINING	35	31	1
14BJ1D5815	H4001	ADVANCED UNIX PROGRAMMING	36	26	1
14BJ1D5815	H4002	INFORMATION SECURITY	37	29	1
14BJ1D5815	H5801	COMPUTER NETWORKS	36	24	1
14BJ1D5815	H5803	COMPILER DESIGN ELECTIVE-I	35	1	0
14BJ1D5815	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	37	25	1
14BJ1D5815	H5806	CSE LAB-2	35	55	1
14BJ1D5816	H0501	DATA WAREHOUSING AND DATA MINING	36	37	1
14BJ1D5816	H4001	ADVANCED UNIX PROGRAMMING	36	34	1
14BJ1D5816	H4002	INFORMATION SECURITY	38	33	1
14BJ1D5816	H5801	COMPUTER NETWORKS	36	24	1
14BJ1D5816	H5803	COMPILER DESIGN ELECTIVE-I	35	-1	0
14BJ1D5816	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	37	24	1
14BJ1D5816	H5806	CSE LAB-2	36	56	1
14BJ1D5817	H0501	DATA WAREHOUSING AND DATA MINING	36	29	1
14BJ1D5817	H4001	ADVANCED UNIX PROGRAMMING	36	24	1
14BJ1D5817	H4002	INFORMATION SECURITY	35	30	1
14BJ1D5817	H5801	COMPUTER NETWORKS	34	24	1
14BJ1D5817	H5803	COMPILER DESIGN ELECTIVE-I	35	11	0
14BJ1D5817	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	36	24	1
14BJ1D5817	H5806	CSE LAB-2	35	52	1
14BJ1D8701	H8701	FINITE ELEMENT METHOD	36	28	1
14BJ1D8701	H8702	EARTHQUAKE RESISTANT DESIGN	33	28	1
14BJ1D8701	H8703	STABILITY OF STRUCTURES	36	0	0
14BJ1D8701	H8704	THEORY OF PLATES & SHELLS	35	45	1
14BJ1D8701	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	36	55	1
14BJ1D8701	H8709	BRIDGE ENGINEERING ELECTIVE-IV	34	30	1
14BJ1D8701	H8711	CAD LAB	39	58	1
14BJ1D8702	H8701	FINITE ELEMENT METHOD	37	26	1
14BJ1D8702	H8702	EARTHQUAKE RESISTANT DESIGN	37	31	1
14BJ1D8702	H8703	STABILITY OF STRUCTURES	36	7	0
14BJ1D8702	H8704	THEORY OF PLATES & SHELLS	37	16	0
14BJ1D8702	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	37	56	1
14BJ1D8702	H8709	BRIDGE ENGINEERING ELECTIVE-IV	38	31	1
14BJ1D8702	H8711	CAD LAB	39	57	1
14BJ1D8703	H8701	FINITE ELEMENT METHOD	37	26	1
14BJ1D8703	H8702	EARTHQUAKE RESISTANT DESIGN	37	48	1
14BJ1D8703	H8703	STABILITY OF STRUCTURES	37	25	1
14BJ1D8703	H8704	THEORY OF PLATES & SHELLS	37	48	1
14BJ1D8703	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	36	59	1
14BJ1D8703	H8709	BRIDGE ENGINEERING ELECTIVE-IV	38	51	1
14BJ1D8703	H8711	CAD LAB	36	56	1
14BJ1D8704	H8701	FINITE ELEMENT METHOD	38	28	1
14BJ1D8704	H8702	EARTHQUAKE RESISTANT DESIGN	37	36	1
14BJ1D8704	H8703	STABILITY OF STRUCTURES	37	14	0
14BJ1D8704	H8704	THEORY OF PLATES & SHELLS	38	43	1

Htno	Subcode	Subname	Internal	External	credits
14BJ1D8704	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	37	28	1
14BJ1D8704	H8709	BRIDGE ENGINEERING ELECTIVE-IV	37	28	1
14BJ1D8704	H8711	CAD LAB	37	57	1
14BJ1D8705	H8701	FINITE ELEMENT METHOD	38	24	1
14BJ1D8705	H8702	EARTHQUAKE RESISTANT DESIGN	38	40	1
14BJ1D8705	H8703	STABILITY OF STRUCTURES	38	0	0
14BJ1D8705	H8704	THEORY OF PLATES & SHELLS	37	35	1
14BJ1D8705	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	34	50	1
14BJ1D8705	H8709	BRIDGE ENGINEERING ELECTIVE-IV	36	24	1
14BJ1D8705	H8711	CAD LAB	36	58	1
14BJ1D8706	H8701	FINITE ELEMENT METHOD	37	24	1
14BJ1D8706	H8702	EARTHQUAKE RESISTANT DESIGN	36	39	1
14BJ1D8706	H8703	STABILITY OF STRUCTURES	37	10	0
14BJ1D8706	H8704	THEORY OF PLATES & SHELLS	36	37	1
14BJ1D8706	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	36	32	1
14BJ1D8706	H8709	BRIDGE ENGINEERING ELECTIVE-IV	38	25	1
14BJ1D8706	H8711	CAD LAB	38	55	1
14BJ1D8707	H8701	FINITE ELEMENT METHOD	37	24	1
14BJ1D8707	H8702	EARTHQUAKE RESISTANT DESIGN	37	39	1
14BJ1D8707	H8703	STABILITY OF STRUCTURES	38	24	1
14BJ1D8707	H8704	THEORY OF PLATES & SHELLS	38	27	1
14BJ1D8707	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	36	27	1
14BJ1D8707	H8709	BRIDGE ENGINEERING ELECTIVE-IV	37	41	1
14BJ1D8707	H8711	CAD LAB	36	57	1
14BJ1D8708	H8701	FINITE ELEMENT METHOD	37	28	1
14BJ1D8708	H8702	EARTHQUAKE RESISTANT DESIGN	38	24	1
14BJ1D8708	H8704	THEORY OF PLATES & SHELLS	38	24	1
14BJ1D8708	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	38	37	1
14BJ1D8708	H8709	BRIDGE ENGINEERING ELECTIVE-IV	37	27	1
14BJ1D8708	H8711	CAD LAB	39	59	1
14BJ1D8709	H8701	FINITE ELEMENT METHOD	37	24	1
14BJ1D8709	H8702	EARTHQUAKE RESISTANT DESIGN	36	5	0
14BJ1D8709	H8703	STABILITY OF STRUCTURES	36	3	0
14BJ1D8709	H8704	THEORY OF PLATES & SHELLS	37	24	1
14BJ1D8709	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	36	24	1
14BJ1D8709	H8709	BRIDGE ENGINEERING ELECTIVE-IV	37	24	1
14BJ1D8709	H8711	CAD LAB	37	58	1
14BJ1D8710	H8701	FINITE ELEMENT METHOD	38	26	1
14BJ1D8710	H8702	EARTHQUAKE RESISTANT DESIGN	37	24	1
14BJ1D8710	H8703	STABILITY OF STRUCTURES	38	8	0
14BJ1D8710	H8704	THEORY OF PLATES & SHELLS	36	26	1
14BJ1D8710	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	35	24	1
14BJ1D8710	H8709	BRIDGE ENGINEERING ELECTIVE-IV	37	34	1
14BJ1D8710	H8711	CAD LAB	38	57	1
14BJ1D8711	H8701	FINITE ELEMENT METHOD	37	-1	0
14BJ1D8711	H8702	EARTHQUAKE RESISTANT DESIGN	37	2	0
14BJ1D8711	H8703	STABILITY OF STRUCTURES	37	10	0
14BJ1D8711	H8704	THEORY OF PLATES & SHELLS	37	24	1
14BJ1D8711	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	37	1	0
14BJ1D8711	H8709	BRIDGE ENGINEERING ELECTIVE-IV	36	29	1
14BJ1D8711	H8711	CAD LAB	36	60	1

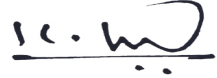
Htno	Subcode	Subname	Internal	External	credits
14BJ1D8712	H8701	FINITE ELEMENT METHOD	37	24	1
14BJ1D8712	H8702	EARTHQUAKE RESISTANT DESIGN	36	5	0
14BJ1D8712	H8703	STABILITY OF STRUCTURES	37	4	0
14BJ1D8712	H8704	THEORY OF PLATES & SHELLS	36	2	0
14BJ1D8712	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	36	17	0
14BJ1D8712	H8709	BRIDGE ENGINEERING ELECTIVE-IV	36	6	0
14BJ1D8712	H8711	CAD LAB	37	55	1
14BJ1D8713	H8701	FINITE ELEMENT METHOD	38	5	0
14BJ1D8713	H8702	EARTHQUAKE RESISTANT DESIGN	36	24	1
14BJ1D8713	H8703	STABILITY OF STRUCTURES	37	4	0
14BJ1D8713	H8704	THEORY OF PLATES & SHELLS	36	24	1
14BJ1D8713	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	36	37	1
14BJ1D8713	H8709	BRIDGE ENGINEERING ELECTIVE-IV	37	24	1
14BJ1D8713	H8711	CAD LAB	38	59	1
14BJ1D8714	H8701	FINITE ELEMENT METHOD	37	-1	0
14BJ1D8714	H8702	EARTHQUAKE RESISTANT DESIGN	37	-1	0
14BJ1D8714	H8703	STABILITY OF STRUCTURES	38	-1	0
14BJ1D8714	H8704	THEORY OF PLATES & SHELLS	37	-1	0
14BJ1D8714	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	38	-1	0
14BJ1D8714	H8709	BRIDGE ENGINEERING ELECTIVE-IV	37	-1	0
14BJ1D8714	H8711	CAD LAB	39	-1	0
14BJ1D8715	H8701	FINITE ELEMENT METHOD	37	25	1
14BJ1D8715	H8702	EARTHQUAKE RESISTANT DESIGN	37	34	1
14BJ1D8715	H8703	STABILITY OF STRUCTURES	38	14	0
14BJ1D8715	H8704	THEORY OF PLATES & SHELLS	37	28	1
14BJ1D8715	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	37	33	1
14BJ1D8715	H8709	BRIDGE ENGINEERING ELECTIVE-IV	38	35	1
14BJ1D8715	H8711	CAD LAB	39	58	1
14BJ1D8716	H8701	FINITE ELEMENT METHOD	35	11	0
14BJ1D8716	H8702	EARTHQUAKE RESISTANT DESIGN	35	20	0
14BJ1D8716	H8703	STABILITY OF STRUCTURES	36	7	0
14BJ1D8716	H8704	THEORY OF PLATES & SHELLS	37	10	0
14BJ1D8716	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	36	26	1
14BJ1D8716	H8709	BRIDGE ENGINEERING ELECTIVE-IV	36	15	0
14BJ1D8716	H8711	CAD LAB	37	57	1
14BJ1D8717	H8701	FINITE ELEMENT METHOD	36	24	1
14BJ1D8717	H8702	EARTHQUAKE RESISTANT DESIGN	37	24	1
14BJ1D8717	H8703	STABILITY OF STRUCTURES	37	24	1
14BJ1D8717	H8704	THEORY OF PLATES & SHELLS	36	18	0
14BJ1D8717	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	37	24	1
14BJ1D8717	H8709	BRIDGE ENGINEERING ELECTIVE-IV	37	31	1
14BJ1D8717	H8711	CAD LAB	38	56	1
14BJ1D8718	H8701	FINITE ELEMENT METHOD	37	25	1
14BJ1D8718	H8702	EARTHQUAKE RESISTANT DESIGN	38	30	1
14BJ1D8718	H8703	STABILITY OF STRUCTURES	37	13	0
14BJ1D8718	H8704	THEORY OF PLATES & SHELLS	37	26	1
14BJ1D8718	H8705	PRE-STRESSED CONCRETE ELECTIVE-III	37	34	1
14BJ1D8718	H8709	BRIDGE ENGINEERING ELECTIVE-IV	36	29	1
14BJ1D8718	H8711	CAD LAB	38	55	1

****Note:1)For Recounting/Revaluation/Challenge By Revaluation Apply through College PORTAL by Examination Incharge only)**

****NOTE:2 [Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: 23-02-2016]**

****NOTE:3 [Please inform to the students enter these subject codes for applying Recounting/Revaluation/Challenge By Revaluation]**

Date:15-02-2016



Controller of Examinations