



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for III B.Tech II Semester (R07,r05,rr) regular/supply Examinations April-2013

College: ST.MARY'S GROUP OF INSTITUTIONS, CHEBROLU, GUNTUR:BJ

Htno	Subcode	Subname	Internal	External	credits
07BJ1A0236	V3210	SWITCH GEAR & PROTECTION	13	16	0
07BJ1A0236	V3218	DIGITAL SIGNAL PROCESSING	6	-1	0
07BJ1A0236	V3219	VLSI DESIGN	11	32	4
07BJ1A0237	V3210	SWITCH GEAR & PROTECTION	11	18	0
07BJ1A0239	V3219	VLSI DESIGN	15	28	4
07BJ1A0245	V3209	INSTRUMENTATION	10	36	4
07BJ1A0259	V3219	VLSI DESIGN	14	-1	0
07BJ1A0436	V3218	DIGITAL SIGNAL PROCESSING	16	33	4
07BJ1A0436	V3219	VLSI DESIGN	14	30	4
07BJ1A0453	V3220	MICROWAVE ENGINEERING	15	32	4
07BJ1A1244	V3224	COMPUTER NETWORKS	16	8	0
07BJ1A1244	V3227	OBJECT ORIENTED ANALYSIS AND DESIGN	16	34	4
07BJ1A1244	V3244	DATA WAREHOUSING AND DATA MINING	17	0	0
08BJ1A0244	V3207	MICRO PROCESSORS AND MICRO CONTROLLERS	14	-1	0
08BJ1A0244	V3219	VLSI DESIGN	14	38	4
08BJ1A0404	V3219	VLSI DESIGN	12	38	4
08BJ1A0422	V3219	VLSI DESIGN	15	3	0
08BJ1A0422	V3221	MICRO PROCESSORS AND INTERFACING	14	15	0
08BJ1A0431	V3219	VLSI DESIGN	12	18	0
08BJ1A0431	V3220	MICROWAVE ENGINEERING	11	19	0
08BJ1A0431	V3221	MICRO PROCESSORS AND INTERFACING	11	9	0
08BJ1A0440	V3218	DIGITAL SIGNAL PROCESSING	11	37	4
08BJ1A0440	V3219	VLSI DESIGN	13	4	0
08BJ1A0440	V3220	MICROWAVE ENGINEERING	11	36	4
08BJ1A0440	V3221	MICRO PROCESSORS AND INTERFACING	11	10	0
08BJ1A0443	V3219	VLSI DESIGN	14	28	4
08BJ1A0445	V3220	MICROWAVE ENGINEERING	12	29	4
08BJ1A0450	V3208	MANAGEMENT SCIENCE	7	38	4
08BJ1A0450	V3217	TELECOMMUNICATION SWITCHING SYSTEMS	7	8	0
08BJ1A0450	V3218	DIGITAL SIGNAL PROCESSING	7	33	4
08BJ1A0450	V3219	VLSI DESIGN	7	40	4
08BJ1A0450	V3220	MICROWAVE ENGINEERING	7	39	4
08BJ1A0450	V3221	MICRO PROCESSORS AND INTERFACING	7	36	4
08BJ1A0506	V3223	COMPILER DESIGN	7	-1	0
08BJ1A0506	V3226	ARTIFICIAL INTELLIGENCE AND NEURAL NETWORKS	10	-1	0
08BJ1A0510	V3224	COMPUTER NETWORKS	14	35	4
08BJ1A0521	V3224	COMPUTER NETWORKS	16	35	4
08BJ1A0531	V3222	OPERATING SYSTEMS	13	10	0
08BJ1A0554	V3222	OPERATING SYSTEMS	15	7	0
08BJ1A0560	V3222	OPERATING SYSTEMS	8	20	0
08BJ1A1218	V3224	COMPUTER NETWORKS	12	2	0
08BJ1A1234	V3227	OBJECT ORIENTED ANALYSIS AND DESIGN	17	44	4
08BJ1A1234	V3243	MIDDLEWARE TECHNOLOGIES	19	40	4
08BJ1A1236	V3242	E-COMMERCE	9	0	0

Htno	Subcode	Subname	Internal	External	credits
08BJ1A1236	V3243	MIDDLEWARE TECHNOLOGIES	19	19	0
08BJ1A1237	V3224	COMPUTER NETWORKS	9	35	4
08BJ1A1237	V3227	OBJECT ORIENTED ANALYSIS AND DESIGN	5	44	4
08BJ1A1237	V3241	WEB TECHNOLOGIES	7	40	4
08BJ1A1237	V3243	MIDDLEWARE TECHNOLOGIES	7	28	0
08BJ1A1247	V3243	MIDDLEWARE TECHNOLOGIES	17	17	0
08BJ1A1249	V3224	COMPUTER NETWORKS	15	14	0
08BJ1A1249	V3241	WEB TECHNOLOGIES	11	30	4
08BJ1A1255	V3242	E-COMMERCE	8	0	0
08BJ1A1255	V3243	MIDDLEWARE TECHNOLOGIES	19	28	4
08BJ5A0201	V3219	VLSI DESIGN	11	35	4
08BJ5A0502	V3227	OBJECT ORIENTED ANALYSIS AND DESIGN	15	43	4
08D01A05E3	V3224	COMPUTER NETWORKS	15	40	4
09761A0350	V3212	CAD/CAM	-1	43	4
09761A0350	V3213	METROLOGY AND SURFACE ENGINEERING	-1	28	0
09761A0350	V3214	REFRIGERATION AND AIR CONDITION	-1	17	0
09761A0350	V3215	DESIGN OF MACHINE MEMBERS - II	-1	28	0
09BJ1A0206	V3207	MICRO PROCESSORS AND MICRO CONTROLLERS	17	43	4
09BJ1A0210	V3218	DIGITAL SIGNAL PROCESSING	14	34	4
09BJ1A0217	V3218	DIGITAL SIGNAL PROCESSING	16	28	4
09BJ1A0220	V3218	DIGITAL SIGNAL PROCESSING	17	35	4
09BJ1A0229	V3218	DIGITAL SIGNAL PROCESSING	12	33	4
09BJ1A0232	V3218	DIGITAL SIGNAL PROCESSING	13	41	4
09BJ1A0302	V3213	METROLOGY AND SURFACE ENGINEERING	16	17	0
09BJ1A0303	V3213	METROLOGY AND SURFACE ENGINEERING	18	42	4
09BJ1A0303	V3215	DESIGN OF MACHINE MEMBERS - II	16	7	0
09BJ1A0303	V3216	THERMAL ENGINEERING- II	19	28	4
09BJ1A0304	V3212	CAD/CAM	18	50	4
09BJ1A0307	V3211	INDUSTRIAL MANAGEMENT	7	-1	0
09BJ1A0307	V3212	CAD/CAM	7	-1	0
09BJ1A0307	V3213	METROLOGY AND SURFACE ENGINEERING	7	-1	0
09BJ1A0307	V3214	REFRIGERATION AND AIR CONDITION	7	-1	0
09BJ1A0307	V3215	DESIGN OF MACHINE MEMBERS - II	7	-1	0
09BJ1A0307	V3216	THERMAL ENGINEERING- II	7	-1	0
09BJ1A0308	V3212	CAD/CAM	15	45	4
09BJ1A0309	V3213	METROLOGY AND SURFACE ENGINEERING	15	35	4
09BJ1A0311	V3213	METROLOGY AND SURFACE ENGINEERING	16	28	4
09BJ1A0311	V3215	DESIGN OF MACHINE MEMBERS - II	14	31	4
09BJ1A0311	V3216	THERMAL ENGINEERING- II	16	28	4
09BJ1A0312	V3213	METROLOGY AND SURFACE ENGINEERING	15	40	4
09BJ1A0312	V3215	DESIGN OF MACHINE MEMBERS - II	15	28	4
09BJ1A0313	V3213	METROLOGY AND SURFACE ENGINEERING	16	37	4
09BJ1A0315	V3214	REFRIGERATION AND AIR CONDITION	14	36	4
09BJ1A0315	V3215	DESIGN OF MACHINE MEMBERS - II	16	28	4
09BJ1A0320	V3213	METROLOGY AND SURFACE ENGINEERING	15	18	0
09BJ1A0320	V3215	DESIGN OF MACHINE MEMBERS - II	13	38	4
09BJ1A0322	V3216	THERMAL ENGINEERING- II	19	38	4
09BJ1A0326	V3213	METROLOGY AND SURFACE ENGINEERING	15	28	4
09BJ1A0326	V3214	REFRIGERATION AND AIR CONDITION	14	33	4
09BJ1A0326	V3215	DESIGN OF MACHINE MEMBERS - II	16	38	4
09BJ1A0326	V3216	THERMAL ENGINEERING- II	16	34	4

Htno	Subcode	Subname	Internal	External	credits
09BJ1A0329	V3213	METROLOGY AND SURFACE ENGINEERING	17	40	4
09BJ1A0331	V3213	METROLOGY AND SURFACE ENGINEERING	16	28	4
09BJ1A0331	V3214	REFRIGERATION AND AIR CONDITION	11	13	0
09BJ1A0331	V3215	DESIGN OF MACHINE MEMBERS - II	12	14	0
09BJ1A0331	V3216	THERMAL ENGINEERING- II	12	29	4
09BJ1A0334	V3213	METROLOGY AND SURFACE ENGINEERING	13	16	0
09BJ1A0335	V3213	METROLOGY AND SURFACE ENGINEERING	14	14	0
09BJ1A0335	V3214	REFRIGERATION AND AIR CONDITION	16	36	4
09BJ1A0339	V3213	METROLOGY AND SURFACE ENGINEERING	14	36	4
09BJ1A0341	V3214	REFRIGERATION AND AIR CONDITION	15	40	4
09BJ1A0341	V3216	THERMAL ENGINEERING- II	16	31	4
09BJ1A0342	V3212	CAD/CAM	13	28	4
09BJ1A0342	V3213	METROLOGY AND SURFACE ENGINEERING	12	8	0
09BJ1A0342	V3215	DESIGN OF MACHINE MEMBERS - II	9	6	0
09BJ1A0342	V3216	THERMAL ENGINEERING- II	14	36	4
09BJ1A0343	V3213	METROLOGY AND SURFACE ENGINEERING	15	35	4
09BJ1A0343	V3216	THERMAL ENGINEERING- II	15	37	4
09BJ1A0405	V3218	DIGITAL SIGNAL PROCESSING	16	-1	0
09BJ1A0405	V3221	MICRO PROCESSORS AND INTERFACING	16	30	4
09BJ1A0409	V3219	VLSI DESIGN	16	38	4
09BJ1A0410	V3219	VLSI DESIGN	16	42	4
09BJ1A0413	V3217	TELECOMMUNICATION SWITCHING SYSTEMS	16	35	4
09BJ1A0413	V3218	DIGITAL SIGNAL PROCESSING	15	38	4
09BJ1A0414	V3219	VLSI DESIGN	16	29	4
09BJ1A0415	V3218	DIGITAL SIGNAL PROCESSING	15	34	4
09BJ1A0418	V3217	TELECOMMUNICATION SWITCHING SYSTEMS	17	28	4
09BJ1A0418	V3219	VLSI DESIGN	18	31	4
09BJ1A0420	V3219	VLSI DESIGN	15	17	0
09BJ1A0422	V3217	TELECOMMUNICATION SWITCHING SYSTEMS	17	0	0
09BJ1A0422	V3221	MICRO PROCESSORS AND INTERFACING	14	29	4
09BJ1A0427	V3217	TELECOMMUNICATION SWITCHING SYSTEMS	15	36	4
09BJ1A0427	V3220	MICROWAVE ENGINEERING	14	17	0
09BJ1A0427	V3221	MICRO PROCESSORS AND INTERFACING	17	34	4
09BJ1A0428	V3219	VLSI DESIGN	15	38	4
09BJ1A0428	V3221	MICRO PROCESSORS AND INTERFACING	17	18	0
09BJ1A0430	V3219	VLSI DESIGN	19	32	4
09BJ1A0430	V3221	MICRO PROCESSORS AND INTERFACING	14	30	4
09BJ1A0431	V3208	MANAGEMENT SCIENCE	7	20	0
09BJ1A0431	V3217	TELECOMMUNICATION SWITCHING SYSTEMS	9	34	4
09BJ1A0431	V3218	DIGITAL SIGNAL PROCESSING	10	19	0
09BJ1A0431	V3219	VLSI DESIGN	7	28	0
09BJ1A0431	V3220	MICROWAVE ENGINEERING	9	8	0
09BJ1A0431	V3221	MICRO PROCESSORS AND INTERFACING	7	28	0
09BJ1A0431	V3266	ADVANCED ENGLISH COMMUNICATION SKILLS LAB	14	42	2
09BJ1A0431	V3269	MICRO PROCESSORS AND INTERFACING LAB.	20	42	2
09BJ1A0434	V3217	TELECOMMUNICATION SWITCHING SYSTEMS	16	18	0
09BJ1A0434	V3219	VLSI DESIGN	19	38	4
09BJ1A0434	V3220	MICROWAVE ENGINEERING	17	19	0
09BJ1A0434	V3221	MICRO PROCESSORS AND INTERFACING	18	36	4
09BJ1A0437	V3217	TELECOMMUNICATION SWITCHING SYSTEMS	13	35	4
09BJ1A0437	V3219	VLSI DESIGN	17	29	4

Htno	Subcode	Subname	Internal	External	credits
09BJ1A0437	V3221	MICRO PROCESSORS AND INTERFACING	17	34	4
09BJ1A0438	V3217	TELECOMMUNICATION SWITCHING SYSTEMS	16	35	4
09BJ1A0438	V3219	VLSI DESIGN	19	39	4
09BJ1A0446	V3217	TELECOMMUNICATION SWITCHING SYSTEMS	16	48	4
09BJ1A0446	V3218	DIGITAL SIGNAL PROCESSING	13	32	4
09BJ1A0446	V3221	MICRO PROCESSORS AND INTERFACING	16	18	0
09BJ1A0448	V3217	TELECOMMUNICATION SWITCHING SYSTEMS	16	28	4
09BJ1A0448	V3218	DIGITAL SIGNAL PROCESSING	13	43	4
09BJ1A0448	V3219	VLSI DESIGN	17	40	4
09BJ1A0450	V3218	DIGITAL SIGNAL PROCESSING	16	35	4
09BJ1A0452	V3217	TELECOMMUNICATION SWITCHING SYSTEMS	16	35	4
09BJ1A0452	V3218	DIGITAL SIGNAL PROCESSING	16	29	4
09BJ1A0452	V3219	VLSI DESIGN	18	42	4
09BJ1A0454	V3219	VLSI DESIGN	17	30	4
09BJ1A0454	V3221	MICRO PROCESSORS AND INTERFACING	16	29	4
09BJ1A0455	V3218	DIGITAL SIGNAL PROCESSING	17	42	4
09BJ1A0457	V3217	TELECOMMUNICATION SWITCHING SYSTEMS	15	34	4
09BJ1A0457	V3218	DIGITAL SIGNAL PROCESSING	16	38	4
09BJ1A0457	V3219	VLSI DESIGN	19	39	4
09BJ1A0513	V3222	OPERATING SYSTEMS	15	-1	0
09BJ1A0513	V3224	COMPUTER NETWORKS	17	29	4
09BJ1A0514	V3224	COMPUTER NETWORKS	12	35	4
09BJ1A0514	V3226	ARTIFICIAL INTELLIGENCE AND NEURAL NETWORKS	15	23	0
09BJ1A0515	V3226	ARTIFICIAL INTELLIGENCE AND NEURAL NETWORKS	14	29	4
09BJ1A0518	V3222	OPERATING SYSTEMS	15	33	4
09BJ1A0521	V3224	COMPUTER NETWORKS	13	31	4
09BJ1A0524	V3222	OPERATING SYSTEMS	18	35	4
09BJ1A0527	V3222	OPERATING SYSTEMS	9	14	0
09BJ1A0527	V3223	COMPILER DESIGN	14	9	0
09BJ1A0527	V3224	COMPUTER NETWORKS	16	34	4
09BJ1A0527	V3226	ARTIFICIAL INTELLIGENCE AND NEURAL NETWORKS	14	38	4
09BJ1A0527	V3227	OBJECT ORIENTED ANALYSIS AND DESIGN	11	51	4
09BJ1A0528	V3224	COMPUTER NETWORKS	15	30	4
09BJ1A0529	V3222	OPERATING SYSTEMS	17	17	0
09BJ1A0537	V3222	OPERATING SYSTEMS	12	12	0
09BJ1A0537	V3223	COMPILER DESIGN	15	19	0
09BJ1A0537	V3224	COMPUTER NETWORKS	17	12	0
09BJ1A0537	V3226	ARTIFICIAL INTELLIGENCE AND NEURAL NETWORKS	16	23	0
09BJ1A0539	V3222	OPERATING SYSTEMS	14	13	0
09BJ1A0539	V3226	ARTIFICIAL INTELLIGENCE AND NEURAL NETWORKS	14	22	0
09BJ1A0539	V3227	OBJECT ORIENTED ANALYSIS AND DESIGN	11	42	4
09BJ1A0545	V3222	OPERATING SYSTEMS	8	28	0
09BJ1A0545	V3223	COMPILER DESIGN	13	21	0
09BJ1A0546	V3225	INFORMATION SECURITY	13	16	0
09BJ1A0546	V3226	ARTIFICIAL INTELLIGENCE AND NEURAL NETWORKS	16	30	4
09BJ1A0549	V3222	OPERATING SYSTEMS	15	29	4
09BJ1A0549	V3226	ARTIFICIAL INTELLIGENCE AND NEURAL NETWORKS	14	41	4
09BJ1A0550	V3222	OPERATING SYSTEMS	11	16	0
09BJ1A0550	V3224	COMPUTER NETWORKS	15	38	4
09BJ1A0555	V3222	OPERATING SYSTEMS	11	11	0
09BJ1A0555	V3223	COMPILER DESIGN	15	17	0

Htno	Subcode	Subname	Internal	External	credits
09BJ1A0555	V3224	COMPUTER NETWORKS	14	39	4
09BJ1A0555	V3226	ARTIFICIAL INTELLIGENCE AND NEURAL NETWORKS	13	28	4
09BJ1A0556	V3222	OPERATING SYSTEMS	10	33	4
09BJ1A0556	V3226	ARTIFICIAL INTELLIGENCE AND NEURAL NETWORKS	14	34	4
09BJ1A0558	V3222	OPERATING SYSTEMS	15	14	0
09BJ1A0558	V3224	COMPUTER NETWORKS	14	37	4
09BJ1A0558	V3226	ARTIFICIAL INTELLIGENCE AND NEURAL NETWORKS	15	38	4
09BJ5A0404	V3208	MANAGEMENT SCIENCE	15	6	0
09BJ5A0404	V3217	TELECOMMUNICATION SWITCHING SYSTEMS	15	-1	0
09BJ5A0404	V3218	DIGITAL SIGNAL PROCESSING	18	18	0
09BJ5A0404	V3219	VLSI DESIGN	15	17	0
09BJ5A0404	V3220	MICROWAVE ENGINEERING	14	6	0
09BJ5A0404	V3221	MICRO PROCESSORS AND INTERFACING	14	18	0
10BJ5A0201	V3218	DIGITAL SIGNAL PROCESSING	15	28	4
10BJ5A0202	V3218	DIGITAL SIGNAL PROCESSING	12	31	4
10BJ5A0301	V3214	REFRIGERATION AND AIR CONDITION	14	23	0
10BJ5A0307	V3213	METROLOGY AND SURFACE ENGINEERING	16	15	0
10BJ5A0308	V3215	DESIGN OF MACHINE MEMBERS - II	16	41	4
10BJ5A0401	V3219	VLSI DESIGN	19	-1	0
10BJ5A0401	V3220	MICROWAVE ENGINEERING	15	40	4
10BJ5A0501	V3226	ARTIFICIAL INTELLIGENCE AND NEURAL NETWORKS	13	48	4

[Last Date for Recounting/Revaluation/Challenge By Revaluation: 27-06-2013]

Date:13-06-2013



Controller of Examinations