



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for III B.Tech I(R07,R05,RR) Semester Supplementary Examinations April-2013

College: ST.MARY'S GROUP OF INSTITUTIONS, CHEBROLU, GUNTUR:BJ

Htno	Subcode	Subname	Internal	External	credits
07BJ1A0236	V3107	LINEAR SYSTEM ANALYSIS(NEW)	0	-1	0
07BJ1A0236	V3108	COMPUTER SYSTEM ORGANIZATION	0	14	0
07BJ1A0236	V3109	ELECTRICAL MEASUREMENTS	0	-1	0
07BJ1A0236	V3110	POWER SYSTEMS-II	0	-1	0
07BJ1A0236	V3111	ELECTRICAL MACHINES-III	0	-1	0
07BJ1A0236	V3112	POWER ELECTRONICS	0	-1	0
07BJ1A0236	V3163	CONTROL SYSTEMS AND SIMULATION LAB	15	40	2
07BJ1A0236	V3164	ELECTRICAL MACHINES LAB " II	15	42	2
07BJ1A0239	V3112	POWER ELECTRONICS	15	8	0
07BJ1A0245	V3108	COMPUTER SYSTEM ORGANIZATION	10	16	0
07BJ1A0245	V3109	ELECTRICAL MEASUREMENTS	12	11	0
07BJ1A0259	V3112	POWER ELECTRONICS	14	-1	0
07BJ1A0436	V3120	LINEAR IC APPLICATIONS	19	-1	0
07BJ1A0453	V3118	ANTENNAS AND WAVE PROPAGATION	16	52	4
07BJ1A1229	V3124	DATA COMMUNICATION SYSTEMS	16	-1	0
07BJ1A1229	V3128	MICRO PROCESSORS AND INTERFACING	13	-1	0
07BJ1A1229	V3147	AUTOMATA AND COMPILER DESIGN	15	-1	0
07BJ1A1244	V3124	DATA COMMUNICATION SYSTEMS	15	9	0
07BJ1A1244	V3125	COMPUTER GRAPHICS	14	7	0
07BJ1A1244	V3126	SOFTWARE TESTING METHODOLOGIES	14	0	0
07BJ1A1244	V3128	MICRO PROCESSORS AND INTERFACING	14	8	0
07BJ1A1244	V3146	DISTRIBUTED DATABASES	15	0	0
07BJ1A1244	V3147	AUTOMATA AND COMPILER DESIGN	13	6	0
08BJ1A0211	V3111	ELECTRICAL MACHINES-III	17	34	4
08BJ1A0214	V3112	POWER ELECTRONICS	15	36	4
08BJ1A0215	V3111	ELECTRICAL MACHINES-III	13	0	0
08BJ1A0244	V3112	POWER ELECTRONICS	17	-1	0
08BJ1A0415	V3118	ANTENNAS AND WAVE PROPAGATION	18	37	4
08BJ1A0421	V3120	LINEAR IC APPLICATIONS	14	16	0
08BJ1A0421	V3121	DIGITAL IC APPLICATIONS	15	13	0
08BJ1A0422	V3120	LINEAR IC APPLICATIONS	16	7	0
08BJ1A0440	V3118	ANTENNAS AND WAVE PROPAGATION	13	20	0
08BJ1A0440	V3120	LINEAR IC APPLICATIONS	12	4	0
08BJ1A0440	V3121	DIGITAL IC APPLICATIONS	14	29	4
08BJ1A0441	V3118	ANTENNAS AND WAVE PROPAGATION	15	38	4
08BJ1A0448	V3118	ANTENNAS AND WAVE PROPAGATION	17	57	4
08BJ1A0450	V3118	ANTENNAS AND WAVE PROPAGATION	7	48	4
08BJ1A0450	V3120	LINEAR IC APPLICATIONS	7	20	0
08BJ1A0450	V3121	DIGITAL IC APPLICATIONS	7	48	4
08BJ1A0458	V3118	ANTENNAS AND WAVE PROPAGATION	11	37	4
08BJ1A0458	V3121	DIGITAL IC APPLICATIONS	11	16	0
08BJ1A0506	V3123	DESIGN AND ANALYSIS OF ALGORITHMS	5	-1	0
08BJ1A0506	V3126	SOFTWARE TESTING METHODOLOGIES	6	-1	0
08BJ1A0506	V3128	MICRO PROCESSORS AND INTERFACING	9	-1	0

Htno	Subcode	Subname	Internal	External	credits
08BJ1A0510	V3126	SOFTWARE TESTING METHODOLOGIES	14	48	4
08BJ1A0522	V3125	COMPUTER GRAPHICS	5	13	0
08BJ1A0522	V3126	SOFTWARE TESTING METHODOLOGIES	6	34	4
08BJ1A0522	V3128	MICRO PROCESSORS AND INTERFACING	7	26	0
08BJ1A0527	V3125	COMPUTER GRAPHICS	10	47	4
08BJ1A0527	V3126	SOFTWARE TESTING METHODOLOGIES	16	38	4
08BJ1A0527	V3127	FORMAL LANGUAGES AND AUTOMATA THEORY	14	65	4
08BJ1A0531	V3123	DESIGN AND ANALYSIS OF ALGORITHMS	7	45	4
08BJ1A0531	V3124	DATA COMMUNICATION SYSTEMS	7	52	4
08BJ1A0531	V3128	MICRO PROCESSORS AND INTERFACING	7	38	4
08BJ1A0532	V3123	DESIGN AND ANALYSIS OF ALGORITHMS	5	49	4
08BJ1A0532	V3126	SOFTWARE TESTING METHODOLOGIES	6	16	0
08BJ1A0532	V3128	MICRO PROCESSORS AND INTERFACING	10	42	4
08BJ1A0545	V3126	SOFTWARE TESTING METHODOLOGIES	7	57	4
08BJ1A0545	V3128	MICRO PROCESSORS AND INTERFACING	2	39	4
08BJ1A0554	V3127	FORMAL LANGUAGES AND AUTOMATA THEORY	9	53	4
08BJ1A0560	V3128	MICRO PROCESSORS AND INTERFACING	12	32	4
08BJ1A1203	V3125	COMPUTER GRAPHICS	13	12	0
08BJ1A1234	V3125	COMPUTER GRAPHICS	16	15	0
08BJ1A1236	V3147	AUTOMATA AND COMPILER DESIGN	12	-1	0
08BJ1A1237	V3125	COMPUTER GRAPHICS	11	-1	0
08BJ1A1237	V3128	MICRO PROCESSORS AND INTERFACING	11	10	0
08BJ1A1247	V3125	COMPUTER GRAPHICS	14	29	4
08BJ1A1249	V3128	MICRO PROCESSORS AND INTERFACING	14	35	4
08BJ1A1255	V3124	DATA COMMUNICATION SYSTEMS	11	-1	0
08BJ1A1255	V3126	SOFTWARE TESTING METHODOLOGIES	13	-1	0
08BJ1A1255	V3146	DISTRIBUTED DATABASES	9	-1	0
08BJ1A1255	V3147	AUTOMATA AND COMPILER DESIGN	9	-1	0
08BJ5A0404	V3122	DIGITAL COMMUNICATIONS	14	41	4
08D01A05E3	V3124	DATA COMMUNICATION SYSTEMS	12	17	0
08DO1A0521	V3126	SOFTWARE TESTING METHODOLOGIES	-1	46	4
09761A0350	V3114	HEAT TRANSFER	2	30	0
09761A0350	V3116	DYNAMICS OF MACHINERY	3	1	0
09BJ1A0210	V3112	POWER ELECTRONICS	15	4	0
09BJ1A0217	V3110	POWER SYSTEMS-II	13	19	0
09BJ1A0220	V3111	ELECTRICAL MACHINES-III	14	48	4
09BJ1A0229	V3111	ELECTRICAL MACHINES-III	13	44	4
09BJ1A0230	V3107	LINEAR SYSTEM ANALYSIS(NEW)	16	36	4
09BJ1A0302	V3101	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	16	42	4
09BJ1A0302	V3113	DESIGN OF MACHINE MEMBERS-I	13	41	4
09BJ1A0302	V3114	HEAT TRANSFER	16	33	4
09BJ1A0302	V3116	DYNAMICS OF MACHINERY	16	0	0
09BJ1A0303	V3116	DYNAMICS OF MACHINERY	15	7	0
09BJ1A0304	V3114	HEAT TRANSFER	14	44	4
09BJ1A0304	V3116	DYNAMICS OF MACHINERY	15	0	0
09BJ1A0307	V3101	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	16	-1	0
09BJ1A0307	V3114	HEAT TRANSFER	15	-1	0
09BJ1A0307	V3115	AUTOMOBILE ENGINEERING	13	-1	0
09BJ1A0307	V3116	DYNAMICS OF MACHINERY	15	-1	0
09BJ1A0307	V3117	MACHINE TOOLS	13	-1	0
09BJ1A0308	V3114	HEAT TRANSFER	14	41	4

Htno	Subcode	Subname	Internal	External	credits
09BJ1A0309	V3113	DESIGN OF MACHINE MEMBERS-I	15	47	4
09BJ1A0309	V3117	MACHINE TOOLS	12	31	4
09BJ1A0312	V3113	DESIGN OF MACHINE MEMBERS-I	13	42	4
09BJ1A0312	V3114	HEAT TRANSFER	13	16	0
09BJ1A0312	V3117	MACHINE TOOLS	11	19	0
09BJ1A0313	V3114	HEAT TRANSFER	13	44	4
09BJ1A0313	V3116	DYNAMICS OF MACHINERY	12	4	0
09BJ1A0315	V3113	DESIGN OF MACHINE MEMBERS-I	15	56	4
09BJ1A0315	V3114	HEAT TRANSFER	15	31	4
09BJ1A0315	V3116	DYNAMICS OF MACHINERY	14	12	0
09BJ1A0320	V3113	DESIGN OF MACHINE MEMBERS-I	13	40	4
09BJ1A0320	V3116	DYNAMICS OF MACHINERY	11	0	0
09BJ1A0320	V3117	MACHINE TOOLS	13	19	0
09BJ1A0322	V3116	DYNAMICS OF MACHINERY	19	11	0
09BJ1A0323	V3113	DESIGN OF MACHINE MEMBERS-I	17	57	4
09BJ1A0323	V3114	HEAT TRANSFER	17	38	4
09BJ1A0326	V3113	DESIGN OF MACHINE MEMBERS-I	13	54	4
09BJ1A0331	V3113	DESIGN OF MACHINE MEMBERS-I	15	37	4
09BJ1A0331	V3114	HEAT TRANSFER	15	33	4
09BJ1A0331	V3116	DYNAMICS OF MACHINERY	13	0	0
09BJ1A0331	V3117	MACHINE TOOLS	12	16	0
09BJ1A0334	V3113	DESIGN OF MACHINE MEMBERS-I	14	40	4
09BJ1A0334	V3114	HEAT TRANSFER	16	28	4
09BJ1A0334	V3116	DYNAMICS OF MACHINERY	12	0	0
09BJ1A0335	V3113	DESIGN OF MACHINE MEMBERS-I	15	45	4
09BJ1A0335	V3114	HEAT TRANSFER	16	32	4
09BJ1A0339	V3113	DESIGN OF MACHINE MEMBERS-I	15	56	4
09BJ1A0339	V3116	DYNAMICS OF MACHINERY	10	0	0
09BJ1A0340	V3113	DESIGN OF MACHINE MEMBERS-I	16	60	4
09BJ1A0341	V3113	DESIGN OF MACHINE MEMBERS-I	14	38	4
09BJ1A0341	V3114	HEAT TRANSFER	13	28	4
09BJ1A0342	V3101	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	13	31	4
09BJ1A0342	V3113	DESIGN OF MACHINE MEMBERS-I	11	46	4
09BJ1A0342	V3114	HEAT TRANSFER	15	31	4
09BJ1A0342	V3115	AUTOMOBILE ENGINEERING	7	15	0
09BJ1A0342	V3116	DYNAMICS OF MACHINERY	7	0	0
09BJ1A0342	V3117	MACHINE TOOLS	9	31	4
09BJ1A0343	V3113	DESIGN OF MACHINE MEMBERS-I	12	51	4
09BJ1A0413	V3120	LINEAR IC APPLICATIONS	12	20	0
09BJ1A0414	V3120	LINEAR IC APPLICATIONS	16	13	0
09BJ1A0415	V3118	ANTENNAS AND WAVE PROPAGATION	14	48	4
09BJ1A0417	V3120	LINEAR IC APPLICATIONS	17	12	0
09BJ1A0418	V3118	ANTENNAS AND WAVE PROPAGATION	15	46	4
09BJ1A0418	V3120	LINEAR IC APPLICATIONS	15	36	4
09BJ1A0418	V3121	DIGITAL IC APPLICATIONS	17	30	4
09BJ1A0426	V3120	LINEAR IC APPLICATIONS	16	14	0
09BJ1A0427	V3101	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	15	44	4
09BJ1A0427	V3120	LINEAR IC APPLICATIONS	14	11	0
09BJ1A0431	V3101	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	12	-1	0
09BJ1A0431	V3118	ANTENNAS AND WAVE PROPAGATION	11	-1	0
09BJ1A0431	V3119	COMPUTER ORGANIZATION	10	-1	0

Htno	Subcode	Subname	Internal	External	credits
09BJ1A0431	V3120	LINEAR IC APPLICATIONS	14	7	0
09BJ1A0431	V3121	DIGITAL IC APPLICATIONS	16	-1	0
09BJ1A0431	V3167	DIGITAL COMMUNICATIONS LAB.	15	40	2
09BJ1A0431	V3168	IC APPLICATIONS AND ECAD LAB.	15	42	2
09BJ1A0434	V3119	COMPUTER ORGANIZATION	15	37	4
09BJ1A0437	V3118	ANTENNAS AND WAVE PROPAGATION	12	39	4
09BJ1A0446	V3120	LINEAR IC APPLICATIONS	15	20	0
09BJ1A0448	V3120	LINEAR IC APPLICATIONS	18	8	0
09BJ1A0507	V3124	DATA COMMUNICATION SYSTEMS	15	15	0
09BJ1A0513	V3128	MICRO PROCESSORS AND INTERFACING	12	36	4
09BJ1A0514	V3126	SOFTWARE TESTING METHODOLOGIES	15	38	4
09BJ1A0515	V3127	FORMAL LANGUAGES AND AUTOMATA THEORY	15	33	4
09BJ1A0515	V3128	MICRO PROCESSORS AND INTERFACING	12	40	4
09BJ1A0527	V3124	DATA COMMUNICATION SYSTEMS	9	11	0
09BJ1A0527	V3125	COMPUTER GRAPHICS	10	30	4
09BJ1A0527	V3128	MICRO PROCESSORS AND INTERFACING	10	38	4
09BJ1A0528	V3126	SOFTWARE TESTING METHODOLOGIES	14	39	4
09BJ1A0528	V3128	MICRO PROCESSORS AND INTERFACING	11	33	4
09BJ1A0537	V3124	DATA COMMUNICATION SYSTEMS	16	5	0
09BJ1A0537	V3126	SOFTWARE TESTING METHODOLOGIES	12	42	4
09BJ1A0537	V3128	MICRO PROCESSORS AND INTERFACING	10	36	4
09BJ1A0539	V3128	MICRO PROCESSORS AND INTERFACING	11	39	4
09BJ1A0546	V3124	DATA COMMUNICATION SYSTEMS	16	3	0
09BJ1A0546	V3126	SOFTWARE TESTING METHODOLOGIES	13	-1	0
09BJ1A0546	V3127	FORMAL LANGUAGES AND AUTOMATA THEORY	14	18	0
09BJ1A0549	V3126	SOFTWARE TESTING METHODOLOGIES	16	-1	0
09BJ1A0550	V3128	MICRO PROCESSORS AND INTERFACING	11	43	4
09BJ1A0551	V3126	SOFTWARE TESTING METHODOLOGIES	17	-1	0
09BJ1A0554	V3124	DATA COMMUNICATION SYSTEMS	15	12	0
09BJ1A0554	V3125	COMPUTER GRAPHICS	13	16	0
09BJ1A0554	V3128	MICRO PROCESSORS AND INTERFACING	10	30	4
09BJ1A0555	V3124	DATA COMMUNICATION SYSTEMS	13	45	4
09BJ1A0555	V3125	COMPUTER GRAPHICS	14	14	0
09BJ1A0555	V3127	FORMAL LANGUAGES AND AUTOMATA THEORY	12	57	4
09BJ1A0555	V3128	MICRO PROCESSORS AND INTERFACING	11	49	4
09BJ1A0556	V3125	COMPUTER GRAPHICS	14	11	0
09BJ1A0556	V3128	MICRO PROCESSORS AND INTERFACING	11	39	4
09BJ5A0404	V3101	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	15	-1	0
09BJ5A0404	V3118	ANTENNAS AND WAVE PROPAGATION	14	34	4
09BJ5A0404	V3119	COMPUTER ORGANIZATION	15	14	0
09BJ5A0404	V3120	LINEAR IC APPLICATIONS	15	9	0
09BJ5A0404	V3121	DIGITAL IC APPLICATIONS	17	29	4
09BJ5A0404	V3122	DIGITAL COMMUNICATIONS	16	28	4
10BJ5A0201	V3107	LINEAR SYSTEM ANALYSIS(NEW)	15	34	4
10BJ5A0306	V3114	HEAT TRANSFER	15	51	4
10BJ5A0306	V3116	DYNAMICS OF MACHINERY	12	3	0
10BJ5A0307	V3116	DYNAMICS OF MACHINERY	12	5	0
10BJ5A0308	V3114	HEAT TRANSFER	15	34	4
10BJ5A0309	V3116	DYNAMICS OF MACHINERY	15	9	0
10BJ5A0310	V3114	HEAT TRANSFER	15	37	4
10BJ5A0317	V3113	DESIGN OF MACHINE MEMBERS-I	19	-1	0

Htno	Subcode	Subname	Internal	External	credits
10BJ5A0401	V3118	ANTENNAS AND WAVE PROPAGATION	8	41	4
10BJ5A0401	V3120	LINEAR IC APPLICATIONS	8	10	0
10BJ5A0401	V3121	DIGITAL IC APPLICATIONS	8	15	0
10BJ5A0402	V3120	LINEAR IC APPLICATIONS	18	10	0

Note:1)For Recounting/Revaluation do the Online registration and send the total amount through online transfer

2)Take Seperate DD for the Challenge Valuation

[Last Date for Recounting/Revaluation/Challenge By Revaluation: 09-07-2013]

Date:05-07-2013



Controller of Examinations