



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for M.Tech II SEMESTER (R16 / R13) Regular / Supplementary Examinations , JUNE- 2018 .

College: ST.MARYS GROUP OF INSTITUTIONS, CHEBROLU, GUNTUR:BJ

Discrepancy pertaining to these results are to be submitted on or before 24-09-2018 with following documents at CE(PG) Office,JNTUK,Kakinada

Htno	Subcode	Subname	Internal	External	credits
14BJ1D0407	H0401	SIMULATION MODELING OF MANUFACTURING SYS	38	-1	0
14BJ1D0407	H0408	INTELLIGENT MANUFACTURING SYSTEMS	37	-1	0
14BJ1D0410	H0401	SIMULATION MODELING OF MANUFACTURING SYS	37	-1	0
14BJ1D0412	H0401	SIMULATION MODELING OF MANUFACTURING SYS	38	6	0
14BJ1D0412	H0402	COMPUTER GRAPHICS	39	27	1
14BJ1D0509	H0501	DATA WAREHOUSING AND DATA MINING	35	27	1
14BJ1D0509	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	36	35	1
14BJ1D0510	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	38	36	1
14BJ1D2105	H2103	FINITE ELEMENT METHOD	38	19	0
14BJ1D2113	H2103	FINITE ELEMENT METHOD	36	13	0
14BJ1D2118	H2103	FINITE ELEMENT METHOD	38	8	0
14BJ1D4305	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	35	27	1
14BJ1D5517	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	34	4	0
14BJ1D5811	H4002	INFORMATION SECURITY	36	25	1
15BJ1D0409	H0402	COMPUTER GRAPHICS	36	28	1
15BJ1D0409	H1501	OPTIMIZATION AND RELIABILITY	38	17	0
15BJ1D0409	H2103	FINITE ELEMENT METHOD	38	5	0
15BJ1D0501	H0503	SOTWARE ENGINEERING	36	39	1
15BJ1D0501	H0505	ADVANCED COMPUTER ARCHITECTURE ELECTIVE-	37	27	1
15BJ1D0501	H4002	INFORMATION SECURITY	36	40	1
15BJ1D0509	H0502	DESIGN AND ANALYSIS OF ALGORITHMS	37	29	1
15BJ1D0509	H4002	INFORMATION SECURITY	36	28	1
15BJ1D0513	H0503	SOTWARE ENGINEERING	33	26	1
15BJ1D0513	H0505	ADVANCED COMPUTER ARCHITECTURE ELECTIVE-	30	13	0
15BJ1D2107	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	30	26	1
15BJ1D2111	H2101	FUELSCOMBUSTION & ENVIRONMENT	32	37	1
15BJ1D2112	H2104	COMPUTATIONAL FLUID DYNAMICS	36	5	0
15BJ1D2114	H2101	FUELSCOMBUSTION & ENVIRONMENT	31	30	1
15BJ1D2114	H2111	JET PROPULSION AND ROCKETRY ELECTIVE-IV	29	25	1
15BJ1D4309	H4301	SWITCHED MODE POWER CONVERSION	31	7	0
15BJ1D4309	H4303	DIGITAL CONTROLLERS	31	14	0
15BJ1D4309	H4304	CUSTOM POWER DEVICES	31	12	0
15BJ1D4309	H4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	32	-1	0
15BJ1D4317	H4301	SWITCHED MODE POWER CONVERSION	36	25	1
15BJ1D5513	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	38	-1	0
15BJ1D5805	H0501	DATA WAREHOUSING AND DATA MINING	35	-1	0
15BJ1D5805	H4001	ADVANCED UNIX PROGRAMMING	31	-1	0
15BJ1D5805	H4002	INFORMATION SECURITY	36	-1	0
15BJ1D5805	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	33	-1	0
15BJ1D5807	H4001	ADVANCED UNIX PROGRAMMING	37	13	0
15BJ1D5807	H4002	INFORMATION SECURITY	35	17	0

Htno	Subcode	Subname	Internal	External	credits
15BJ1D5807	H5801	COMPUTER NETWORKS	34	11	0
15BJ1D5807	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	38	27	1
15BJ1D8712	H8701	FINITE ELEMENT METHOD	37	-1	0
16BJ1D0401	J0401	MODELING & SIMULATION OF MANUFACTURING S	26	-1	0
16BJ1D0401	J0402	COMPUTER GRAPHICS	26	18	0
16BJ1D0401	J0405	CONCURRENT ENGINEERING ELECTIVEIII	28	16	0
16BJ1D0401	J0407	INTELLIGENT MANUFACTURING SYSTEMS	25	-1	0
16BJ1D0401	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	20	34	1
16BJ1D0401	J1501	OPTIMIZATION AND RELIABILITY	26	-1	0
16BJ1D0401	J2103	FINITE ELEMENT METHODS	25	24	0
16BJ1D0402	J2103	FINITE ELEMENT METHODS	37	33	1
16BJ1D0406	J0401	MODELING & SIMULATION OF MANUFACTURING S	29	6	0
16BJ1D0406	J0407	INTELLIGENT MANUFACTURING SYSTEMS	29	11	0
16BJ1D0406	J1501	OPTIMIZATION AND RELIABILITY	29	8	0
16BJ1D0406	J2103	FINITE ELEMENT METHODS	30	1	0
16BJ1D0501	J2503	CYBER SECURITY	37	24	1
16BJ1D0501	J2510	CLOUD COMPUTING ELECTIVE II	39	24	1
16BJ1D0505	J2503	CYBER SECURITY	31	25	1
16BJ1D0506	J2503	CYBER SECURITY	30	0	0
16BJ1D0506	J4001	ADVANCED UNIX PROGRAMMING	30	27	1
16BJ1D2101	J2101	FUELS COMBUSTION & ENVIRONMENT	34	28	1
16BJ1D2101	J2103	FINITE ELEMENT METHODS	35	8	0
16BJ1D2101	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	36	0	0
16BJ1D2101	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	36	17	0
16BJ1D2102	J2111	JET PROPULSION AND ROCKETRY ELECTIVEIV	36	29	1
16BJ1D2104	J2101	FUELS COMBUSTION & ENVIRONMENT	35	32	1
16BJ1D2104	J2103	FINITE ELEMENT METHODS	36	24	1
16BJ1D2104	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	36	4	0
16BJ1D2104	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	36	27	1
16BJ1D2105	J2111	JET PROPULSION AND ROCKETRY ELECTIVEIV	39	32	1
16BJ1D4301	J4301	SWITCHED MODE POWER CONVERSION	35	-1	0
16BJ1D4301	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	35	-1	0
16BJ1D4301	J4303	DIGITAL CONTROLLERS	35	-1	0
16BJ1D4301	J4304	CUSTOM POWER DEVICES	35	-1	0
16BJ1D4301	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	35	-1	0
16BJ1D4301	J4308	SPECIAL MACHINES ELECTIVEIV	34	-1	0
16BJ1D4302	J4303	DIGITAL CONTROLLERS	35	24	1
16BJ1D4302	J4304	CUSTOM POWER DEVICES	34	24	1
16BJ1D4302	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	34	27	1
16BJ1D4302	J4308	SPECIAL MACHINES ELECTIVEIV	34	17	0
16BJ1D4304	J4303	DIGITAL CONTROLLERS	35	31	1
16BJ1D4307	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	33	42	1
16BJ1D4308	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	33	12	0
16BJ1D4308	J4304	CUSTOM POWER DEVICES	35	25	1
16BJ1D4308	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	34	34	1
16BJ1D4308	J4308	SPECIAL MACHINES ELECTIVEIV	33	26	1
16BJ1D5504	J6805	DSP PROCESSORS AND ARCHITECTURES	37	30	1
16BJ1D5505	J5501	HARDWARE SOFTWARE CODESIGN	38	11	0
16BJ1D5505	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	38	0	0
16BJ1D5505	J6805	DSP PROCESSORS AND ARCHITECTURES	37	-1	0
16BJ1D5508	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	34	14	0

Htno	Subcode	Subname	Internal	External	credits
16BJ1D5508	J6805	DSP PROCESSORS AND ARCHITECTURES	35	24	1
16BJ1D5510	J5501	HARDWARE SOFTWARE CODESIGN	34	19	0
16BJ1D5511	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	38	28	1
16BJ1D5513	J4506	INTERNET PROTOCOLS ELECTIVEIV	38	24	1
16BJ1D5513	J5501	HARDWARE SOFTWARE CODESIGN	38	25	1
16BJ1D5514	J5501	HARDWARE SOFTWARE CODESIGN	37	14	0
16BJ1D5514	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	37	30	1
16BJ1D5802	J0502	SOFTWARE ENGINEERING ELECTIVE I	35	28	1
16BJ1D5802	J2503	CYBER SECURITY	35	8	0
16BJ1D5802	J4001	ADVANCED UNIX PROGRAMMING	37	34	1
16BJ1D5803	J2503	CYBER SECURITY	34	27	1
16BJ1D5803	J4002	BIG DATA ANALYTICS	32	26	1
16BJ1D5804	J2503	CYBER SECURITY	36	3	0
16BJ1D5804	J2510	CLOUD COMPUTING ELECTIVE II	32	1	0
16BJ1D5804	J4001	ADVANCED UNIX PROGRAMMING	34	10	0
16BJ1D5804	J4002	BIG DATA ANALYTICS	31	26	1
16BJ1D5805	J0502	SOFTWARE ENGINEERING ELECTIVE I	35	10	0
16BJ1D5805	J2503	CYBER SECURITY	37	0	0
16BJ1D5805	J2510	CLOUD COMPUTING ELECTIVE II	36	7	0
16BJ1D5805	J4001	ADVANCED UNIX PROGRAMMING	37	29	1
16BJ1D5805	J4002	BIG DATA ANALYTICS	37	5	0
16BJ1D5805	J5801	COMPUTER NETWORKS	38	0	0
16BJ1D5808	J2503	CYBER SECURITY	33	24	1
16BJ1D5811	J2503	CYBER SECURITY	37	27	1
16BJ1D5812	J0502	SOFTWARE ENGINEERING ELECTIVE I	35	-1	0
16BJ1D5812	J2503	CYBER SECURITY	35	-1	0
16BJ1D5812	J2510	CLOUD COMPUTING ELECTIVE II	35	-1	0
16BJ1D5812	J4001	ADVANCED UNIX PROGRAMMING	31	-1	0
16BJ1D5812	J4002	BIG DATA ANALYTICS	34	-1	0
16BJ1D5812	J5801	COMPUTER NETWORKS	32	-1	0
16BJ1D8701	J8701	FINITE ELEMENT METHODS	37	35	1
16BJ1D8702	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	34	15	0
16BJ1D8702	J8703	STABILITY OF STRUCTURES	34	0	0
16BJ1D8702	J8704	THEORY OF PLATES & SHELLS	33	0	0
16BJ1D8702	J8705	PRESTRESSED CONCRETE ELECTIVEI	34	1	0
16BJ1D8702	J8709	BRIDGE ENGINEERING ELECTIVEII	34	15	0
16BJ1D8705	J8701	FINITE ELEMENT METHODS	33	37	1
16BJ1D8705	J8705	PRESTRESSED CONCRETE ELECTIVEI	35	10	0
16BJ1D8706	J8701	FINITE ELEMENT METHODS	34	37	1
16BJ1D8707	J8701	FINITE ELEMENT METHODS	34	-1	0
16BJ1D8707	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	36	24	1
16BJ1D8707	J8703	STABILITY OF STRUCTURES	35	0	0
16BJ1D8707	J8704	THEORY OF PLATES & SHELLS	34	0	0
16BJ1D8707	J8705	PRESTRESSED CONCRETE ELECTIVEI	35	-1	0
16BJ1D8707	J8709	BRIDGE ENGINEERING ELECTIVEII	35	17	0
16BJ1D8708	J8701	FINITE ELEMENT METHODS	38	37	1
16BJ1D8709	J8701	FINITE ELEMENT METHODS	35	17	0
16BJ1D8710	J8701	FINITE ELEMENT METHODS	34	33	1
16BJ1D8711	J8701	FINITE ELEMENT METHODS	34	12	0
16BJ1D8711	J8705	PRESTRESSED CONCRETE ELECTIVEI	34	26	1
16BJ1D8711	J8709	BRIDGE ENGINEERING ELECTIVEII	34	16	0

Htno	Subcode	Subname	Internal	External	credits
16BJ1D8712	J8701	FINITE ELEMENT METHODS	35	8	0
17BJ1D0401	J0401	MODELING & SIMULATION OF MANUFACTURING S	37	26	1
17BJ1D0401	J0402	COMPUTER GRAPHICS	35	33	1
17BJ1D0401	J0405	CONCURRENT ENGINEERING ELECTIVEIII	35	40	1
17BJ1D0401	J0407	INTELLIGENT MANUFACTURING SYSTEMS	37	31	1
17BJ1D0401	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	35	55	1
17BJ1D0401	J1501	OPTIMIZATION AND RELIABILITY	36	15	0
17BJ1D0401	J2103	FINITE ELEMENT METHODS	34	8	0
17BJ1D0402	J0401	MODELING & SIMULATION OF MANUFACTURING S	36	-1	0
17BJ1D0402	J0402	COMPUTER GRAPHICS	36	-1	0
17BJ1D0402	J0405	CONCURRENT ENGINEERING ELECTIVEIII	35	-1	0
17BJ1D0402	J0407	INTELLIGENT MANUFACTURING SYSTEMS	36	-1	0
17BJ1D0402	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	34	54	1
17BJ1D0402	J1501	OPTIMIZATION AND RELIABILITY	35	-1	0
17BJ1D0402	J2103	FINITE ELEMENT METHODS	35	-1	0
17BJ1D0403	J0401	MODELING & SIMULATION OF MANUFACTURING S	36	6	0
17BJ1D0403	J0402	COMPUTER GRAPHICS	37	33	1
17BJ1D0403	J0405	CONCURRENT ENGINEERING ELECTIVEIII	35	48	1
17BJ1D0403	J0407	INTELLIGENT MANUFACTURING SYSTEMS	37	36	1
17BJ1D0403	J0408	MODELING AND ANALYSIS OF MANUFACTURING P	36	56	1
17BJ1D0403	J1501	OPTIMIZATION AND RELIABILITY	37	30	1
17BJ1D0403	J2103	FINITE ELEMENT METHODS	37	10	0
17BJ1D2102	J2101	FUELS COMBUSTION & ENVIRONMENT	35	27	1
17BJ1D2102	J2102	ENERGY MANAGEMENT	35	30	1
17BJ1D2102	J2103	FINITE ELEMENT METHODS	36	17	0
17BJ1D2102	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	31	0	0
17BJ1D2102	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	34	11	0
17BJ1D2102	J2111	JET PROPULSION AND ROCKETRY ELECTIVEIV	37	0	0
17BJ1D2102	J2113	THERMAL SYSTEMS DESIGN LAB	35	51	1
17BJ1D2103	J2101	FUELS COMBUSTION & ENVIRONMENT	34	42	1
17BJ1D2103	J2102	ENERGY MANAGEMENT	33	30	1
17BJ1D2103	J2103	FINITE ELEMENT METHODS	35	17	0
17BJ1D2103	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	33	9	0
17BJ1D2103	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	32	12	0
17BJ1D2103	J2111	JET PROPULSION AND ROCKETRY ELECTIVEIV	30	27	1
17BJ1D2103	J2113	THERMAL SYSTEMS DESIGN LAB	33	45	1
17BJ1D2104	J2101	FUELS COMBUSTION & ENVIRONMENT	32	0	0
17BJ1D2104	J2102	ENERGY MANAGEMENT	33	24	1
17BJ1D2104	J2103	FINITE ELEMENT METHODS	31	-1	0
17BJ1D2104	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	32	-1	0
17BJ1D2104	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	31	6	0
17BJ1D2104	J2111	JET PROPULSION AND ROCKETRY ELECTIVEIV	32	-1	0
17BJ1D2104	J2113	THERMAL SYSTEMS DESIGN LAB	32	50	1
17BJ1D2105	J2101	FUELS COMBUSTION & ENVIRONMENT	18	30	0
17BJ1D2105	J2102	ENERGY MANAGEMENT	17	9	0
17BJ1D2105	J2103	FINITE ELEMENT METHODS	16	0	0
17BJ1D2105	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	17	0	0
17BJ1D2105	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	18	8	0
17BJ1D2105	J2111	JET PROPULSION AND ROCKETRY ELECTIVEIV	17	17	0
17BJ1D2105	J2113	THERMAL SYSTEMS DESIGN LAB	31	50	1
17BJ1D2106	J2101	FUELS COMBUSTION & ENVIRONMENT	35	51	1

Htno	Subcode	Subname	Internal	External	credits
17BJ1D2106	J2102	ENERGY MANAGEMENT	36	36	1
17BJ1D2106	J2103	FINITE ELEMENT METHODS	36	25	1
17BJ1D2106	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	36	28	1
17BJ1D2106	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	36	37	1
17BJ1D2106	J2111	JET PROPULSION AND ROCKETRY ELECTIVEIV	36	28	1
17BJ1D2106	J2113	THERMAL SYSTEMS DESIGN LAB	36	54	1
17BJ1D2107	J2101	FUELS COMBUSTION & ENVIRONMENT	32	34	1
17BJ1D2107	J2102	ENERGY MANAGEMENT	33	30	1
17BJ1D2107	J2103	FINITE ELEMENT METHODS	31	25	1
17BJ1D2107	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	32	7	0
17BJ1D2107	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	31	-1	0
17BJ1D2107	J2111	JET PROPULSION AND ROCKETRY ELECTIVEIV	31	27	1
17BJ1D2107	J2113	THERMAL SYSTEMS DESIGN LAB	32	48	1
17BJ1D2108	J2101	FUELS COMBUSTION & ENVIRONMENT	32	42	1
17BJ1D2108	J2102	ENERGY MANAGEMENT	30	29	1
17BJ1D2108	J2103	FINITE ELEMENT METHODS	30	10	0
17BJ1D2108	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	25	9	0
17BJ1D2108	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	31	12	0
17BJ1D2108	J2111	JET PROPULSION AND ROCKETRY ELECTIVEIV	30	27	1
17BJ1D2108	J2113	THERMAL SYSTEMS DESIGN LAB	30	48	1
17BJ1D2109	J2101	FUELS COMBUSTION & ENVIRONMENT	32	36	1
17BJ1D2109	J2102	ENERGY MANAGEMENT	31	32	1
17BJ1D2109	J2103	FINITE ELEMENT METHODS	31	28	1
17BJ1D2109	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	29	9	0
17BJ1D2109	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	30	25	1
17BJ1D2109	J2111	JET PROPULSION AND ROCKETRY ELECTIVEIV	32	28	1
17BJ1D2109	J2113	THERMAL SYSTEMS DESIGN LAB	31	45	1
17BJ1D2110	J2101	FUELS COMBUSTION & ENVIRONMENT	35	39	1
17BJ1D2110	J2102	ENERGY MANAGEMENT	34	30	1
17BJ1D2110	J2103	FINITE ELEMENT METHODS	36	16	0
17BJ1D2110	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	35	5	0
17BJ1D2110	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	33	25	1
17BJ1D2110	J2111	JET PROPULSION AND ROCKETRY ELECTIVEIV	36	24	1
17BJ1D2110	J2113	THERMAL SYSTEMS DESIGN LAB	35	54	1
17BJ1D2111	J2101	FUELS COMBUSTION & ENVIRONMENT	33	40	1
17BJ1D2111	J2102	ENERGY MANAGEMENT	33	35	1
17BJ1D2111	J2103	FINITE ELEMENT METHODS	31	16	0
17BJ1D2111	J2104	COMPUTATIONAL FLUID DYNAMICS ELECTIVE-II	31	11	0
17BJ1D2111	J2107	THERMAL AND NUCLEAR POWER PLANTS ELECTI	32	14	0
17BJ1D2111	J2111	JET PROPULSION AND ROCKETRY ELECTIVEIV	33	25	1
17BJ1D2111	J2113	THERMAL SYSTEMS DESIGN LAB	32	50	1
17BJ1D4301	J4301	SWITCHED MODE POWER CONVERSION	37	24	1
17BJ1D4301	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	36	32	1
17BJ1D4301	J4303	DIGITAL CONTROLLERS	35	4	0
17BJ1D4301	J4304	CUSTOM POWER DEVICES	36	24	1
17BJ1D4301	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	36	31	1
17BJ1D4301	J4308	SPECIAL MACHINES ELECTIVEIV	35	24	1
17BJ1D4301	J4310	POWER CONVERTERS & DRIVES LABORATORY	38	56	1
17BJ1D4302	J4301	SWITCHED MODE POWER CONVERSION	35	24	1
17BJ1D4302	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	34	5	0
17BJ1D4302	J4303	DIGITAL CONTROLLERS	34	18	0

Htno	Subcode	Subname	Internal	External	credits
17BJ1D4302	J4304	CUSTOM POWER DEVICES	33	16	0
17BJ1D4302	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	34	32	1
17BJ1D4302	J4308	SPECIAL MACHINES ELECTIVEIV	35	24	1
17BJ1D4302	J4310	POWER CONVERTERS & DRIVES LABORATORY	35	54	1
17BJ1D4303	J4301	SWITCHED MODE POWER CONVERSION	39	45	1
17BJ1D4303	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	39	26	1
17BJ1D4303	J4303	DIGITAL CONTROLLERS	39	25	1
17BJ1D4303	J4304	CUSTOM POWER DEVICES	39	32	1
17BJ1D4303	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	39	57	1
17BJ1D4303	J4308	SPECIAL MACHINES ELECTIVEIV	40	34	1
17BJ1D4303	J4310	POWER CONVERTERS & DRIVES LABORATORY	39	56	1
17BJ1D4304	J4301	SWITCHED MODE POWER CONVERSION	35	25	1
17BJ1D4304	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	35	25	1
17BJ1D4304	J4303	DIGITAL CONTROLLERS	34	12	0
17BJ1D4304	J4304	CUSTOM POWER DEVICES	35	16	0
17BJ1D4304	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	34	43	1
17BJ1D4304	J4308	SPECIAL MACHINES ELECTIVEIV	34	28	1
17BJ1D4304	J4310	POWER CONVERTERS & DRIVES LABORATORY	38	55	1
17BJ1D4306	J4301	SWITCHED MODE POWER CONVERSION	36	8	0
17BJ1D4306	J4302	POWER ELECTRONIC CONTROL OF AC DRIVES	35	10	0
17BJ1D4306	J4303	DIGITAL CONTROLLERS	34	14	0
17BJ1D4306	J4304	CUSTOM POWER DEVICES	35	10	0
17BJ1D4306	J4307	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	35	12	0
17BJ1D4306	J4308	SPECIAL MACHINES ELECTIVEIV	35	5	0
17BJ1D4306	J4310	POWER CONVERTERS & DRIVES LABORATORY	38	55	1
17BJ1D5501	J4506	INTERNET PROTOCOLS ELECTIVEIV	36	32	1
17BJ1D5501	J5501	HARDWARE SOFTWARE CODESIGN	36	16	0
17BJ1D5501	J5502	EMBEDDED NETWORKING	38	28	1
17BJ1D5501	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	36	55	1
17BJ1D5501	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	35	28	1
17BJ1D5501	J6805	DSP PROCESSORS AND ARCHITECTURES	37	2	0
17BJ1D5501	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	37	32	1
17BJ1D5502	J4506	INTERNET PROTOCOLS ELECTIVEIV	37	24	1
17BJ1D5502	J5501	HARDWARE SOFTWARE CODESIGN	34	16	0
17BJ1D5502	J5502	EMBEDDED NETWORKING	36	24	1
17BJ1D5502	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	38	57	1
17BJ1D5502	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	36	-1	0
17BJ1D5502	J6805	DSP PROCESSORS AND ARCHITECTURES	36	0	0
17BJ1D5502	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	30	1
17BJ1D5503	J4506	INTERNET PROTOCOLS ELECTIVEIV	39	28	1
17BJ1D5503	J5501	HARDWARE SOFTWARE CODESIGN	35	24	1
17BJ1D5503	J5502	EMBEDDED NETWORKING	37	37	1
17BJ1D5503	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	36	56	1
17BJ1D5503	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	38	24	1
17BJ1D5503	J6805	DSP PROCESSORS AND ARCHITECTURES	37	0	0
17BJ1D5503	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	38	43	1
17BJ1D5504	J4506	INTERNET PROTOCOLS ELECTIVEIV	36	28	1
17BJ1D5504	J5501	HARDWARE SOFTWARE CODESIGN	34	24	1
17BJ1D5504	J5502	EMBEDDED NETWORKING	36	30	1
17BJ1D5504	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	37	56	1
17BJ1D5504	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	36	24	1

Htno	Subcode	Subname	Internal	External	credits
17BJ1D5504	J6805	DSP PROCESSORS AND ARCHITECTURES	34	4	0
17BJ1D5504	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	40	1
17BJ1D5505	J4506	INTERNET PROTOCOLS ELECTIVEIV	37	8	0
17BJ1D5505	J5501	HARDWARE SOFTWARE CODESIGN	34	14	0
17BJ1D5505	J5502	EMBEDDED NETWORKING	37	6	0
17BJ1D5505	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	35	55	1
17BJ1D5505	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	36	19	0
17BJ1D5505	J6805	DSP PROCESSORS AND ARCHITECTURES	33	7	0
17BJ1D5505	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	34	4	0
17BJ1D5506	J4506	INTERNET PROTOCOLS ELECTIVEIV	36	36	1
17BJ1D5506	J5501	HARDWARE SOFTWARE CODESIGN	35	25	1
17BJ1D5506	J5502	EMBEDDED NETWORKING	40	44	1
17BJ1D5506	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	39	59	1
17BJ1D5506	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	36	26	1
17BJ1D5506	J6805	DSP PROCESSORS AND ARCHITECTURES	36	34	1
17BJ1D5506	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	37	48	1
17BJ1D5507	J4506	INTERNET PROTOCOLS ELECTIVEIV	38	24	1
17BJ1D5507	J5501	HARDWARE SOFTWARE CODESIGN	39	25	1
17BJ1D5507	J5502	EMBEDDED NETWORKING	40	31	1
17BJ1D5507	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	38	56	1
17BJ1D5507	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	38	28	1
17BJ1D5507	J6805	DSP PROCESSORS AND ARCHITECTURES	37	26	1
17BJ1D5507	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	38	1	0
17BJ1D5508	J4506	INTERNET PROTOCOLS ELECTIVEIV	37	36	1
17BJ1D5508	J5501	HARDWARE SOFTWARE CODESIGN	36	34	1
17BJ1D5508	J5502	EMBEDDED NETWORKING	39	45	1
17BJ1D5508	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	37	56	1
17BJ1D5508	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	38	31	1
17BJ1D5508	J6805	DSP PROCESSORS AND ARCHITECTURES	37	28	1
17BJ1D5508	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	42	1
17BJ1D5509	J4506	INTERNET PROTOCOLS ELECTIVEIV	37	24	1
17BJ1D5509	J5501	HARDWARE SOFTWARE CODESIGN	39	24	1
17BJ1D5509	J5502	EMBEDDED NETWORKING	39	30	1
17BJ1D5509	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	38	57	1
17BJ1D5509	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	39	33	1
17BJ1D5509	J6805	DSP PROCESSORS AND ARCHITECTURES	36	4	0
17BJ1D5509	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	33	1
17BJ1D5510	J4506	INTERNET PROTOCOLS ELECTIVEIV	37	28	1
17BJ1D5510	J5501	HARDWARE SOFTWARE CODESIGN	36	28	1
17BJ1D5510	J5502	EMBEDDED NETWORKING	37	26	1
17BJ1D5510	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	37	56	1
17BJ1D5510	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	38	2	0
17BJ1D5510	J6805	DSP PROCESSORS AND ARCHITECTURES	37	24	1
17BJ1D5510	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	10	0
17BJ1D5511	J4506	INTERNET PROTOCOLS ELECTIVEIV	34	28	1
17BJ1D5511	J5501	HARDWARE SOFTWARE CODESIGN	36	24	1
17BJ1D5511	J5502	EMBEDDED NETWORKING	36	30	1
17BJ1D5511	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	36	56	1
17BJ1D5511	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	37	32	1
17BJ1D5511	J6805	DSP PROCESSORS AND ARCHITECTURES	34	24	1
17BJ1D5511	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	34	36	1

Htno	Subcode	Subname	Internal	External	credits
17BJ1D5512	J4506	INTERNET PROTOCOLS ELECTIVEIV	36	32	1
17BJ1D5512	J5501	HARDWARE SOFTWARE CODESIGN	34	33	1
17BJ1D5512	J5502	EMBEDDED NETWORKING	35	28	1
17BJ1D5512	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	37	56	1
17BJ1D5512	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	37	31	1
17BJ1D5512	J6805	DSP PROCESSORS AND ARCHITECTURES	32	26	1
17BJ1D5512	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	33	39	1
17BJ1D5513	J4506	INTERNET PROTOCOLS ELECTIVEIV	38	30	1
17BJ1D5513	J5501	HARDWARE SOFTWARE CODESIGN	38	24	1
17BJ1D5513	J5502	EMBEDDED NETWORKING	38	29	1
17BJ1D5513	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	38	58	1
17BJ1D5513	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	39	14	0
17BJ1D5513	J6805	DSP PROCESSORS AND ARCHITECTURES	38	2	0
17BJ1D5513	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	37	36	1
17BJ1D5514	J4506	INTERNET PROTOCOLS ELECTIVEIV	36	24	1
17BJ1D5514	J5501	HARDWARE SOFTWARE CODESIGN	35	12	0
17BJ1D5514	J5502	EMBEDDED NETWORKING	36	26	1
17BJ1D5514	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	38	57	1
17BJ1D5514	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	37	26	1
17BJ1D5514	J6805	DSP PROCESSORS AND ARCHITECTURES	37	4	0
17BJ1D5514	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	37	39	1
17BJ1D5515	J4506	INTERNET PROTOCOLS ELECTIVEIV	37	24	1
17BJ1D5515	J5501	HARDWARE SOFTWARE CODESIGN	36	19	0
17BJ1D5515	J5502	EMBEDDED NETWORKING	36	27	1
17BJ1D5515	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	38	58	1
17BJ1D5515	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	39	16	0
17BJ1D5515	J6805	DSP PROCESSORS AND ARCHITECTURES	37	2	0
17BJ1D5515	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	38	35	1
17BJ1D5516	J4506	INTERNET PROTOCOLS ELECTIVEIV	38	32	1
17BJ1D5516	J5501	HARDWARE SOFTWARE CODESIGN	35	31	1
17BJ1D5516	J5502	EMBEDDED NETWORKING	37	36	1
17BJ1D5516	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	37	57	1
17BJ1D5516	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	37	31	1
17BJ1D5516	J6805	DSP PROCESSORS AND ARCHITECTURES	37	32	1
17BJ1D5516	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	35	56	1
17BJ1D5517	J4506	INTERNET PROTOCOLS ELECTIVEIV	38	42	1
17BJ1D5517	J5501	HARDWARE SOFTWARE CODESIGN	38	24	1
17BJ1D5517	J5502	EMBEDDED NETWORKING	39	27	1
17BJ1D5517	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	39	59	1
17BJ1D5517	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	39	41	1
17BJ1D5517	J6805	DSP PROCESSORS AND ARCHITECTURES	38	32	1
17BJ1D5517	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	39	44	1
17BJ1D5518	J4506	INTERNET PROTOCOLS ELECTIVEIV	39	26	1
17BJ1D5518	J5501	HARDWARE SOFTWARE CODESIGN	40	24	1
17BJ1D5518	J5502	EMBEDDED NETWORKING	40	45	1
17BJ1D5518	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	38	58	1
17BJ1D5518	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	40	25	1
17BJ1D5518	J6805	DSP PROCESSORS AND ARCHITECTURES	38	30	1
17BJ1D5518	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	38	60	1
17BJ1D5519	J4506	INTERNET PROTOCOLS ELECTIVEIV	36	28	1
17BJ1D5519	J5501	HARDWARE SOFTWARE CODESIGN	37	11	0

Htno	Subcode	Subname	Internal	External	credits
17BJ1D5519	J5502	EMBEDDED NETWORKING	35	27	1
17BJ1D5519	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	35	55	1
17BJ1D5519	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	37	8	0
17BJ1D5519	J6805	DSP PROCESSORS AND ARCHITECTURES	35	0	0
17BJ1D5519	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	30	1
17BJ1D5520	J4506	INTERNET PROTOCOLS ELECTIVEIV	39	38	1
17BJ1D5520	J5501	HARDWARE SOFTWARE CODESIGN	39	28	1
17BJ1D5520	J5502	EMBEDDED NETWORKING	39	37	1
17BJ1D5520	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	39	59	1
17BJ1D5520	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	40	31	1
17BJ1D5520	J6805	DSP PROCESSORS AND ARCHITECTURES	38	42	1
17BJ1D5520	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	39	56	1
17BJ1D5521	J4506	INTERNET PROTOCOLS ELECTIVEIV	39	24	1
17BJ1D5521	J5501	HARDWARE SOFTWARE CODESIGN	38	31	1
17BJ1D5521	J5502	EMBEDDED NETWORKING	39	36	1
17BJ1D5521	J5505	EMBEDDED SYSTEM DESIGN LABORATORY	39	59	1
17BJ1D5521	J5702	SYSTEM ON CHIP DESIGN ELECTIVEIV	39	16	0
17BJ1D5521	J6805	DSP PROCESSORS AND ARCHITECTURES	39	0	0
17BJ1D5521	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	38	60	1
17BJ1D5801	J0502	SOFTWARE ENGINEERING ELECTIVE I	15	-1	0
17BJ1D5801	J2503	CYBER SECURITY	15	-1	0
17BJ1D5801	J2510	CLOUD COMPUTING ELECTIVE II	15	-1	0
17BJ1D5801	J4001	ADVANCED UNIX PROGRAMMING	15	0	0
17BJ1D5801	J4002	BIG DATA ANALYTICS	16	-1	0
17BJ1D5801	J5801	COMPUTER NETWORKS	15	-1	0
17BJ1D5801	J5803	CSE LAB 2	20	50	1
17BJ1D5802	J0502	SOFTWARE ENGINEERING ELECTIVE I	38	27	1
17BJ1D5802	J2503	CYBER SECURITY	35	4	0
17BJ1D5802	J2510	CLOUD COMPUTING ELECTIVE II	39	28	1
17BJ1D5802	J4001	ADVANCED UNIX PROGRAMMING	37	16	0
17BJ1D5802	J4002	BIG DATA ANALYTICS	37	25	1
17BJ1D5802	J5801	COMPUTER NETWORKS	37	27	1
17BJ1D5802	J5803	CSE LAB 2	35	55	1
17BJ1D5803	J0502	SOFTWARE ENGINEERING ELECTIVE I	37	30	1
17BJ1D5803	J2503	CYBER SECURITY	37	36	1
17BJ1D5803	J2510	CLOUD COMPUTING ELECTIVE II	38	28	1
17BJ1D5803	J4001	ADVANCED UNIX PROGRAMMING	37	25	1
17BJ1D5803	J4002	BIG DATA ANALYTICS	37	36	1
17BJ1D5803	J5801	COMPUTER NETWORKS	38	32	1
17BJ1D5803	J5803	CSE LAB 2	37	57	1
17BJ1D5804	J0502	SOFTWARE ENGINEERING ELECTIVE I	38	29	1
17BJ1D5804	J2503	CYBER SECURITY	37	31	1
17BJ1D5804	J2510	CLOUD COMPUTING ELECTIVE II	37	35	1
17BJ1D5804	J4001	ADVANCED UNIX PROGRAMMING	38	39	1
17BJ1D5804	J4002	BIG DATA ANALYTICS	38	44	1
17BJ1D5804	J5801	COMPUTER NETWORKS	37	32	1
17BJ1D5804	J5803	CSE LAB 2	37	56	1
17BJ1D5805	J0502	SOFTWARE ENGINEERING ELECTIVE I	39	31	1
17BJ1D5805	J2503	CYBER SECURITY	39	29	1
17BJ1D5805	J2510	CLOUD COMPUTING ELECTIVE II	39	36	1
17BJ1D5805	J4001	ADVANCED UNIX PROGRAMMING	40	29	1

Htno	Subcode	Subname	Internal	External	credits
17BJ1D5805	J4002	BIG DATA ANALYTICS	39	41	1
17BJ1D5805	J5801	COMPUTER NETWORKS	40	33	1
17BJ1D5805	J5803	CSE LAB 2	39	58	1
17BJ1D5806	J0502	SOFTWARE ENGINEERING ELECTIVE I	38	27	1
17BJ1D5806	J2503	CYBER SECURITY	39	24	1
17BJ1D5806	J2510	CLOUD COMPUTING ELECTIVE II	39	25	1
17BJ1D5806	J4001	ADVANCED UNIX PROGRAMMING	39	32	1
17BJ1D5806	J4002	BIG DATA ANALYTICS	39	27	1
17BJ1D5806	J5801	COMPUTER NETWORKS	39	30	1
17BJ1D5806	J5803	CSE LAB 2	38	58	1
17BJ1D5807	J0502	SOFTWARE ENGINEERING ELECTIVE I	36	29	1
17BJ1D5807	J2503	CYBER SECURITY	37	26	1
17BJ1D5807	J2510	CLOUD COMPUTING ELECTIVE II	38	27	1
17BJ1D5807	J4001	ADVANCED UNIX PROGRAMMING	37	36	1
17BJ1D5807	J4002	BIG DATA ANALYTICS	38	27	1
17BJ1D5807	J5801	COMPUTER NETWORKS	38	24	1
17BJ1D5807	J5803	CSE LAB 2	36	56	1
17BJ1D5808	J0502	SOFTWARE ENGINEERING ELECTIVE I	38	26	1
17BJ1D5808	J2503	CYBER SECURITY	36	0	0
17BJ1D5808	J2510	CLOUD COMPUTING ELECTIVE II	37	33	1
17BJ1D5808	J4001	ADVANCED UNIX PROGRAMMING	38	27	1
17BJ1D5808	J4002	BIG DATA ANALYTICS	38	17	0
17BJ1D5808	J5801	COMPUTER NETWORKS	38	30	1
17BJ1D5808	J5803	CSE LAB 2	35	55	1
17BJ1D5809	J0502	SOFTWARE ENGINEERING ELECTIVE I	40	24	1
17BJ1D5809	J2503	CYBER SECURITY	39	24	1
17BJ1D5809	J2510	CLOUD COMPUTING ELECTIVE II	39	28	1
17BJ1D5809	J4001	ADVANCED UNIX PROGRAMMING	39	24	1
17BJ1D5809	J4002	BIG DATA ANALYTICS	39	24	1
17BJ1D5809	J5801	COMPUTER NETWORKS	39	26	1
17BJ1D5809	J5803	CSE LAB 2	38	57	1
17BJ1D5810	J0502	SOFTWARE ENGINEERING ELECTIVE I	39	29	1
17BJ1D5810	J2503	CYBER SECURITY	38	30	1
17BJ1D5810	J2510	CLOUD COMPUTING ELECTIVE II	39	41	1
17BJ1D5810	J4001	ADVANCED UNIX PROGRAMMING	39	32	1
17BJ1D5810	J4002	BIG DATA ANALYTICS	37	36	1
17BJ1D5810	J5801	COMPUTER NETWORKS	39	30	1
17BJ1D5810	J5803	CSE LAB 2	38	57	1
17BJ1D5811	J0502	SOFTWARE ENGINEERING ELECTIVE I	40	37	1
17BJ1D5811	J2503	CYBER SECURITY	39	39	1
17BJ1D5811	J2510	CLOUD COMPUTING ELECTIVE II	39	42	1
17BJ1D5811	J4001	ADVANCED UNIX PROGRAMMING	38	36	1
17BJ1D5811	J4002	BIG DATA ANALYTICS	39	25	1
17BJ1D5811	J5801	COMPUTER NETWORKS	39	32	1
17BJ1D5811	J5803	CSE LAB 2	39	58	1
17BJ1D8702	J8701	FINITE ELEMENT METHODS	31	0	0
17BJ1D8702	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	29	25	1
17BJ1D8702	J8703	STABILITY OF STRUCTURES	32	12	0
17BJ1D8702	J8704	THEORY OF PLATES & SHELLS	29	1	0
17BJ1D8702	J8705	PRESTRESSED CONCRETE ELECTIVEI	32	24	1
17BJ1D8702	J8709	BRIDGE ENGINEERING ELECTIVEII	28	9	0

Htno	Subcode	Subname	Internal	External	credits
17BJ1D8702	J8711	CAD LABORATORY	38	57	1
17BJ1D8703	J8701	FINITE ELEMENT METHODS	28	0	0
17BJ1D8703	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	28	18	0
17BJ1D8703	J8703	STABILITY OF STRUCTURES	27	6	0
17BJ1D8703	J8704	THEORY OF PLATES & SHELLS	32	0	0
17BJ1D8703	J8705	PRESTRESSED CONCRETE ELECTIVEI	30	1	0
17BJ1D8703	J8709	BRIDGE ENGINEERING ELECTIVEII	29	14	0
17BJ1D8703	J8711	CAD LABORATORY	37	56	1
17BJ1D8704	J8701	FINITE ELEMENT METHODS	18	-1	0
17BJ1D8704	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	16	6	0
17BJ1D8704	J8703	STABILITY OF STRUCTURES	13	-1	0
17BJ1D8704	J8704	THEORY OF PLATES & SHELLS	13	-1	0
17BJ1D8704	J8705	PRESTRESSED CONCRETE ELECTIVEI	16	13	0
17BJ1D8704	J8709	BRIDGE ENGINEERING ELECTIVEII	14	15	0
17BJ1D8704	J8711	CAD LABORATORY	38	56	1
17BJ1D8706	J8701	FINITE ELEMENT METHODS	23	9	0
17BJ1D8706	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	26	13	0
17BJ1D8706	J8703	STABILITY OF STRUCTURES	27	2	0
17BJ1D8706	J8704	THEORY OF PLATES & SHELLS	27	1	0
17BJ1D8706	J8705	PRESTRESSED CONCRETE ELECTIVEI	26	10	0
17BJ1D8706	J8709	BRIDGE ENGINEERING ELECTIVEII	22	8	0
17BJ1D8706	J8711	CAD LABORATORY	37	56	1
17BJ1D8707	J8701	FINITE ELEMENT METHODS	15	0	0
17BJ1D8707	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	16	24	0
17BJ1D8707	J8703	STABILITY OF STRUCTURES	15	3	0
17BJ1D8707	J8704	THEORY OF PLATES & SHELLS	14	2	0
17BJ1D8707	J8705	PRESTRESSED CONCRETE ELECTIVEI	17	16	0
17BJ1D8707	J8709	BRIDGE ENGINEERING ELECTIVEII	17	13	0
17BJ1D8707	J8711	CAD LABORATORY	36	54	1
17BJ1D8708	J8701	FINITE ELEMENT METHODS	33	0	0
17BJ1D8708	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	23	5	0
17BJ1D8708	J8703	STABILITY OF STRUCTURES	30	10	0
17BJ1D8708	J8704	THEORY OF PLATES & SHELLS	32	0	0
17BJ1D8708	J8705	PRESTRESSED CONCRETE ELECTIVEI	29	0	0
17BJ1D8708	J8709	BRIDGE ENGINEERING ELECTIVEII	27	15	0
17BJ1D8708	J8711	CAD LABORATORY	36	57	1
17BJ1D8709	J8701	FINITE ELEMENT METHODS	31	25	1
17BJ1D8709	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	32	28	1
17BJ1D8709	J8703	STABILITY OF STRUCTURES	35	26	1
17BJ1D8709	J8704	THEORY OF PLATES & SHELLS	32	6	0
17BJ1D8709	J8705	PRESTRESSED CONCRETE ELECTIVEI	32	35	1
17BJ1D8709	J8709	BRIDGE ENGINEERING ELECTIVEII	32	24	1
17BJ1D8709	J8711	CAD LABORATORY	38	58	1
17BJ1D8710	J8701	FINITE ELEMENT METHODS	14	-1	0
17BJ1D8710	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	15	-1	0
17BJ1D8710	J8703	STABILITY OF STRUCTURES	16	-1	0
17BJ1D8710	J8704	THEORY OF PLATES & SHELLS	16	-1	0
17BJ1D8710	J8705	PRESTRESSED CONCRETE ELECTIVEI	16	-1	0
17BJ1D8710	J8709	BRIDGE ENGINEERING ELECTIVEII	14	-1	0
17BJ1D8710	J8711	CAD LABORATORY	24	-1	0
17BJ1D8711	J8701	FINITE ELEMENT METHODS	27	4	0

Htno	Subcode	Subname	Internal	External	credits
17BJ1D8711	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	29	6	0
17BJ1D8711	J8703	STABILITY OF STRUCTURES	33	-1	0
17BJ1D8711	J8704	THEORY OF PLATES & SHELLS	32	-1	0
17BJ1D8711	J8705	PRESTRESSED CONCRETE ELECTIVEI	34	14	0
17BJ1D8711	J8709	BRIDGE ENGINEERING ELECTIVEII	28	18	0
17BJ1D8711	J8711	CAD LABORATORY	38	56	1
17BJ1D8712	J8701	FINITE ELEMENT METHODS	37	10	0
17BJ1D8712	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	34	24	1
17BJ1D8712	J8703	STABILITY OF STRUCTURES	34	12	0
17BJ1D8712	J8704	THEORY OF PLATES & SHELLS	33	11	0
17BJ1D8712	J8705	PRESTRESSED CONCRETE ELECTIVEI	32	25	1
17BJ1D8712	J8709	BRIDGE ENGINEERING ELECTIVEII	33	24	1
17BJ1D8712	J8711	CAD LABORATORY	37	57	1
17BJ1D8713	J8701	FINITE ELEMENT METHODS	30	0	0
17BJ1D8713	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	34	24	1
17BJ1D8713	J8703	STABILITY OF STRUCTURES	33	11	0
17BJ1D8713	J8704	THEORY OF PLATES & SHELLS	31	25	1
17BJ1D8713	J8705	PRESTRESSED CONCRETE ELECTIVEI	36	20	0
17BJ1D8713	J8709	BRIDGE ENGINEERING ELECTIVEII	34	24	1
17BJ1D8713	J8711	CAD LABORATORY	39	59	1
17BJ1D8715	J8701	FINITE ELEMENT METHODS	29	-1	0
17BJ1D8715	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	32	24	1
17BJ1D8715	J8703	STABILITY OF STRUCTURES	37	11	0
17BJ1D8715	J8704	THEORY OF PLATES & SHELLS	31	0	0
17BJ1D8715	J8705	PRESTRESSED CONCRETE ELECTIVEI	30	16	0
17BJ1D8715	J8709	BRIDGE ENGINEERING ELECTIVEII	34	14	0
17BJ1D8715	J8711	CAD LABORATORY	37	58	1
17BJ1D8716	J8701	FINITE ELEMENT METHODS	28	17	0
17BJ1D8716	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	22	-1	0
17BJ1D8716	J8703	STABILITY OF STRUCTURES	28	7	0
17BJ1D8716	J8704	THEORY OF PLATES & SHELLS	26	10	0
17BJ1D8716	J8705	PRESTRESSED CONCRETE ELECTIVEI	20	6	0
17BJ1D8716	J8709	BRIDGE ENGINEERING ELECTIVEII	25	15	0
17BJ1D8716	J8711	CAD LABORATORY	36	54	1
17BJ1D8717	J8701	FINITE ELEMENT METHODS	31	5	0
17BJ1D8717	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	31	35	1
17BJ1D8717	J8703	STABILITY OF STRUCTURES	25	3	0
17BJ1D8717	J8704	THEORY OF PLATES & SHELLS	26	9	0
17BJ1D8717	J8705	PRESTRESSED CONCRETE ELECTIVEI	28	15	0
17BJ1D8717	J8709	BRIDGE ENGINEERING ELECTIVEII	27	12	0
17BJ1D8717	J8711	CAD LABORATORY	37	55	1
17BJ1D8718	J8701	FINITE ELEMENT METHODS	26	0	0
17BJ1D8718	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	29	30	1
17BJ1D8718	J8703	STABILITY OF STRUCTURES	30	10	0
17BJ1D8718	J8704	THEORY OF PLATES & SHELLS	30	-1	0
17BJ1D8718	J8705	PRESTRESSED CONCRETE ELECTIVEI	25	-1	0
17BJ1D8718	J8709	BRIDGE ENGINEERING ELECTIVEII	32	6	0
17BJ1D8718	J8711	CAD LABORATORY	36	56	1
17BJ1D8719	J8701	FINITE ELEMENT METHODS	32	0	0
17BJ1D8719	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	29	26	1
17BJ1D8719	J8703	STABILITY OF STRUCTURES	36	10	0

Htno	Subcode	Subname	Internal	External	credits
17BJ1D8719	J8704	THEORY OF PLATES & SHELLS	31	24	1
17BJ1D8719	J8705	PRESTRESSED CONCRETE ELECTIVEI	31	24	1
17BJ1D8719	J8709	BRIDGE ENGINEERING ELECTIVEII	28	21	0
17BJ1D8719	J8711	CAD LABORATORY	37	56	1
17BJ1D8720	J8701	FINITE ELEMENT METHODS	18	-1	0
17BJ1D8720	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	18	-1	0
17BJ1D8720	J8703	STABILITY OF STRUCTURES	18	-1	0
17BJ1D8720	J8704	THEORY OF PLATES & SHELLS	17	-1	0
17BJ1D8720	J8705	PRESTRESSED CONCRETE ELECTIVEI	16	-1	0
17BJ1D8720	J8709	BRIDGE ENGINEERING ELECTIVEII	17	-1	0
17BJ1D8720	J8711	CAD LABORATORY	24	-1	0
17BJ1D8721	J8701	FINITE ELEMENT METHODS	17	-1	0
17BJ1D8721	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	19	8	0
17BJ1D8721	J8703	STABILITY OF STRUCTURES	14	2	0
17BJ1D8721	J8704	THEORY OF PLATES & SHELLS	17	0	0
17BJ1D8721	J8705	PRESTRESSED CONCRETE ELECTIVEI	0	1	0
17BJ1D8721	J8709	BRIDGE ENGINEERING ELECTIVEII	19	-1	0
17BJ1D8721	J8711	CAD LABORATORY	24	-1	0
17BJ1D8722	J8701	FINITE ELEMENT METHODS	37	32	1
17BJ1D8722	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	39	32	1
17BJ1D8722	J8703	STABILITY OF STRUCTURES	37	24	1
17BJ1D8722	J8704	THEORY OF PLATES & SHELLS	37	24	1
17BJ1D8722	J8705	PRESTRESSED CONCRETE ELECTIVEI	37	35	1
17BJ1D8722	J8709	BRIDGE ENGINEERING ELECTIVEII	38	24	1
17BJ1D8722	J8711	CAD LABORATORY	39	59	1
17BJ1D8723	J8701	FINITE ELEMENT METHODS	35	16	0
17BJ1D8723	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	30	35	1
17BJ1D8723	J8703	STABILITY OF STRUCTURES	34	15	0
17BJ1D8723	J8704	THEORY OF PLATES & SHELLS	36	27	1
17BJ1D8723	J8705	PRESTRESSED CONCRETE ELECTIVEI	31	31	1
17BJ1D8723	J8709	BRIDGE ENGINEERING ELECTIVEII	36	24	1
17BJ1D8723	J8711	CAD LABORATORY	37	57	1
17BJ1D8724	J8701	FINITE ELEMENT METHODS	27	-1	0
17BJ1D8724	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	28	-1	0
17BJ1D8724	J8703	STABILITY OF STRUCTURES	30	5	0
17BJ1D8724	J8704	THEORY OF PLATES & SHELLS	30	-1	0
17BJ1D8724	J8705	PRESTRESSED CONCRETE ELECTIVEI	28	-1	0
17BJ1D8724	J8709	BRIDGE ENGINEERING ELECTIVEII	20	-1	0
17BJ1D8724	J8711	CAD LABORATORY	35	-1	0
17BJ1D8725	J8701	FINITE ELEMENT METHODS	35	14	0
17BJ1D8725	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	34	33	1
17BJ1D8725	J8703	STABILITY OF STRUCTURES	37	4	0
17BJ1D8725	J8704	THEORY OF PLATES & SHELLS	34	9	0
17BJ1D8725	J8705	PRESTRESSED CONCRETE ELECTIVEI	33	14	0
17BJ1D8725	J8709	BRIDGE ENGINEERING ELECTIVEII	35	25	1
17BJ1D8725	J8711	CAD LABORATORY	37	58	1
17BJ1D8726	J8701	FINITE ELEMENT METHODS	21	0	0
17BJ1D8726	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	27	31	1
17BJ1D8726	J8703	STABILITY OF STRUCTURES	28	5	0
17BJ1D8726	J8704	THEORY OF PLATES & SHELLS	28	4	0
17BJ1D8726	J8705	PRESTRESSED CONCRETE ELECTIVEI	30	16	0

Htno	Subcode	Subname	Internal	External	credits
17BJ1D8726	J8709	BRIDGE ENGINEERING ELECTIVEII	26	15	0
17BJ1D8726	J8711	CAD LABORATORY	36	56	1

****Note:1)**For Recounting/Revaluation/Challenge By Revaluation Apply through Online(www.jntukresults.edu.in)

****NOTE:2** [Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: **01-10-2018**]

****NOTE:3** [Please inform to the students to enter these subject codes for applying Recounting/Revaluation/Challenge By Revaluation]

****NOTE:**

-1 in the filed of externals indicates student absent for the respective subject.

-2 in the filed of externals indicates student result is withheld for the respective subject.

-3 in the filed of externals indicates Malpractice for the respective subject.]

Date:17-09-2018

N. Mohan Rao
Controller of Examinations